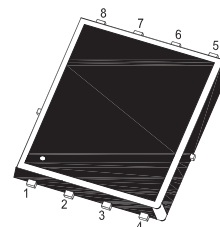
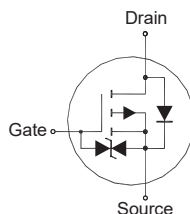


P-Channel Enhancement Mode MOSFET

Features

- High speed switch
- Advanced trench cell design
- Built-in G-S Protection Diode
- Halogen and Antimony Free(HAF), RoHS compliant
- Typical ESD Protection HBM Class 2



1.Source 2.Source 3.Source 4.Gate
5.Drain 6.Drain 7.Drain 8.Drain
DFN5060 Plastic Package

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000

Key Parameters

Parameter	Value	Unit
$-BV_{DS}$	100	V
$R_{DS(ON)} \text{ Max}$	68 @ $-V_{GS} = 10 \text{ V}$	mΩ
	78 @ $-V_{GS} = 4.5 \text{ V}$	
$-V_{GS(th)} \text{ typ}$	1.5	V
$Q_g \text{ typ}$	71 @ $-V_{GS} = 10 \text{ V}$	nC

Applications

- Portable appliances
- Battery management

Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	$-I_D$	18	A
		11	
Peak Drain Current ¹⁾	$-I_{DM}$	80	A
Avalanche Current	$-I_{AS}$	18.2	A
Single Pulse Avalanche Energy ²⁾	E_{AS}	82.8	mJ
Total Power Dissipation	P_{tot}	45.7	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.7	°C/W
Thermal Resistance from Junction to Ambient ³⁾	$R_{\theta JA}$	41	°C/W

¹⁾ Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.

²⁾ Limited by $T_{J(MAX)}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5 \text{ mH}$, $R_g = 25 \Omega$, $-I_{AS} = 18.2 \text{ A}$, $V_{GS} = 10 \text{ V}$.

³⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $-I_D = 250\ \mu\text{A}$	$-BV_{DS}$	100	-	-	V
Drain-Source Leakage Current at $-V_{DS} = 100\ \text{V}$	$-I_{DSS}$	-	-	1	μA
Gate-Source Leakage Current at $V_{GS} = \pm 20\ \text{V}$	I_{GSS}	-	-	± 10	μA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}$, $-I_D = 250\ \mu\text{A}$	$-V_{GS(th)}$	1	-	2.3	V
Drain-Source On-State Resistance at $-V_{GS} = 10\ \text{V}$, $-I_D = 10\ \text{A}$ at $-V_{GS} = 4.5\ \text{V}$, $-I_D = 8\ \text{A}$	$R_{DS(on)}$	- -	55 -	68 78	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Forward Transconductance at $-V_{DS} = 5\ \text{V}$, $-I_D = 8\ \text{A}$	g_{FS}	-	26.3	-	S
Gate resistance at $V_{DS} = 0\ \text{V}$, $V_{GS} = 0\ \text{V}$, $f = 1\ \text{MHz}$	R_g	-	5.1	-	Ω
Input Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 50\ \text{V}$, $f = 1\ \text{MHz}$	C_{iss}	-	4177	-	pF
Output Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 50\ \text{V}$, $f = 1\ \text{MHz}$	C_{oss}	-	95	-	pF
Reverse Transfer Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 50\ \text{V}$, $f = 1\ \text{MHz}$	C_{rss}	-	17	-	pF
Gate charge total at $-V_{DS} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$ at $-V_{DS} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 4.5\ \text{V}$	Q_g	- -	71 33	- -	nC
Gate to Source Charge at $-V_{DS} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$	Q_{gs}	-	12	-	nC
Gate to Drain Charge at $-V_{DS} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$	Q_{gd}	-	9	-	nC
Turn-On Delay Time at $-V_{DD} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$, $R_g = 3.3\ \Omega$	$t_{d(on)}$	-	16	-	ns
Turn-On Rise Time at $-V_{DD} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$, $R_g = 3.3\ \Omega$	t_r	-	20	-	ns
Turn-Off Delay Time at $-V_{DD} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$, $R_g = 3.3\ \Omega$	$t_{d(off)}$	-	50	-	ns
Turn-Off Fall Time at $-V_{DD} = 50\ \text{V}$, $-I_D = 10\ \text{A}$, $-V_{GS} = 10\ \text{V}$, $R_g = 3.3\ \Omega$	t_f	-	9	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $-I_S = 1\ \text{A}$, $V_{GS} = 0\ \text{V}$	$-V_{SD}$	-	-	1.3	V
Body-Diode Continuous Current	$-I_S$	-	-	18	A
Body-Diode Continuous Current, Pulsed	$-I_{SM}$	-	-	80	A
Body Diode Reverse Recovery Time at $-I_S = 10\ \text{A}$, $di/dt = 100\ \text{A} / \mu\text{s}$	t_{rr}	-	28.5	-	ns
Body Diode Reverse Recovery Charge at $-I_S = 10\ \text{A}$, $di/dt = 100\ \text{A} / \mu\text{s}$	Q_{rr}	-	40	-	nC

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

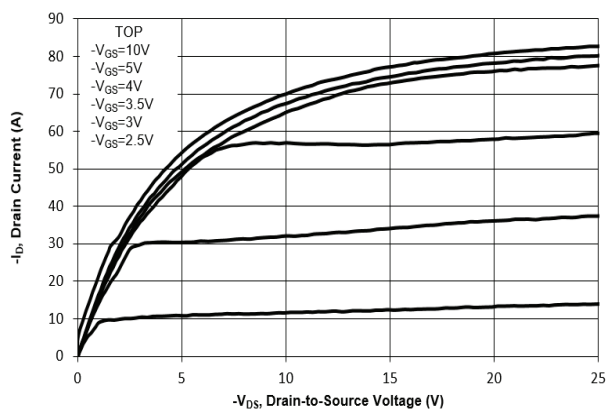


Fig. 2 Typical Transfer Characteristics

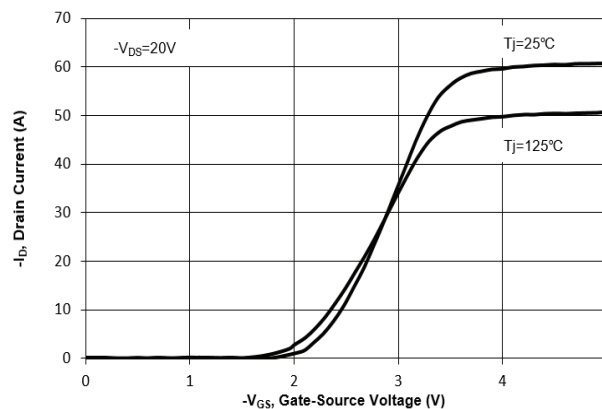


Fig. 3 On-Resistance vs. Drain Current

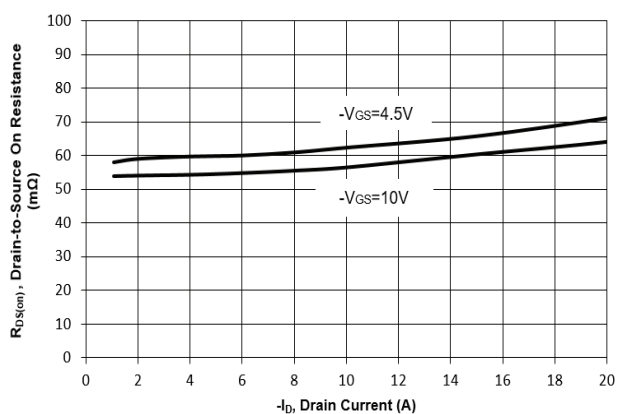


Fig. 4 On-Resistance vs. Gate to Source Voltage

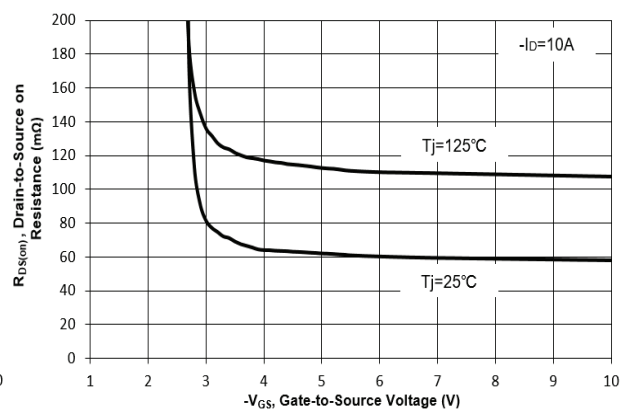


Fig. 5 On-Resistance vs. T_j

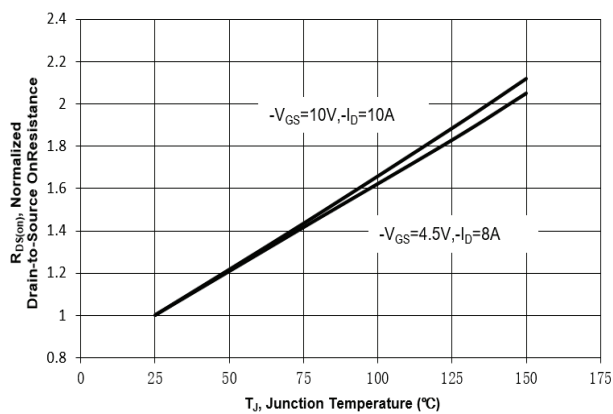
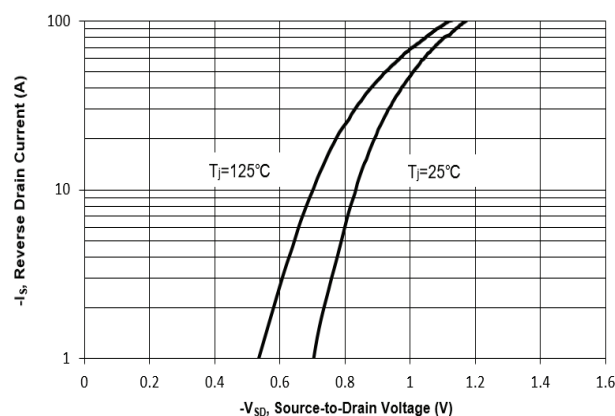


Fig. 6 Typical Body-Diode Forward Characteristics



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

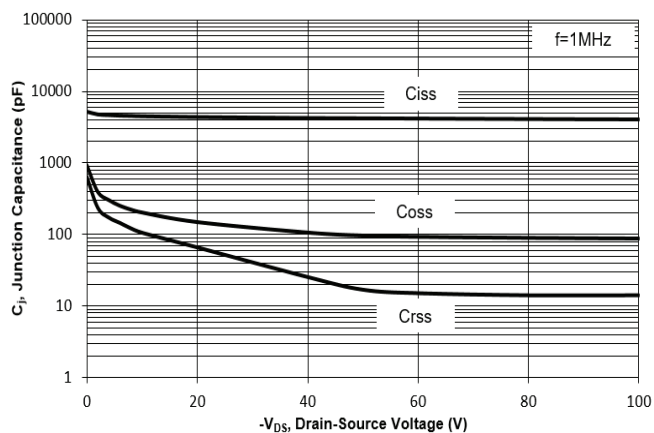


Fig. 8 Drain-Source Leakage Current vs. T_J

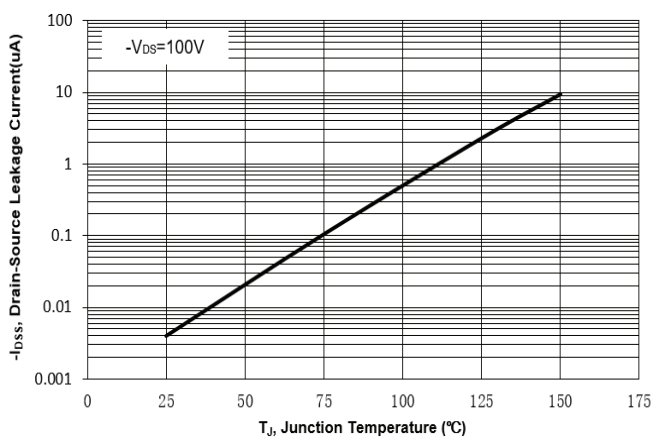


Fig. 9 V_{(BR)DSS} vs. Junction Temperature

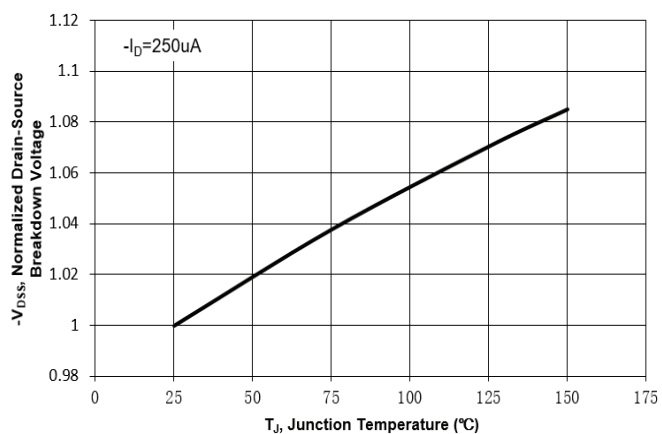


Fig. 10 Gate Threshold Variation vs. T_J

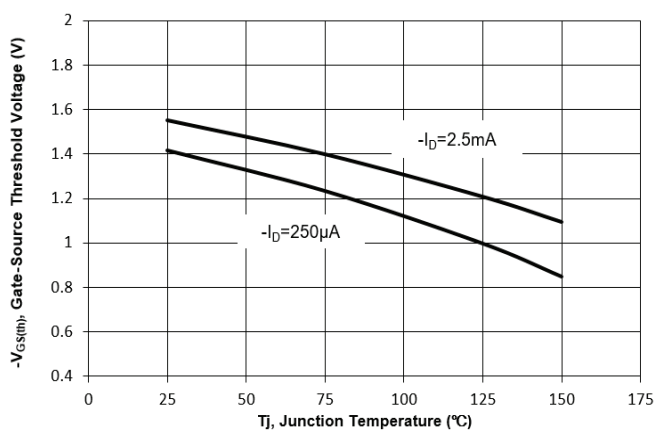


Fig. 11 Gate Charge

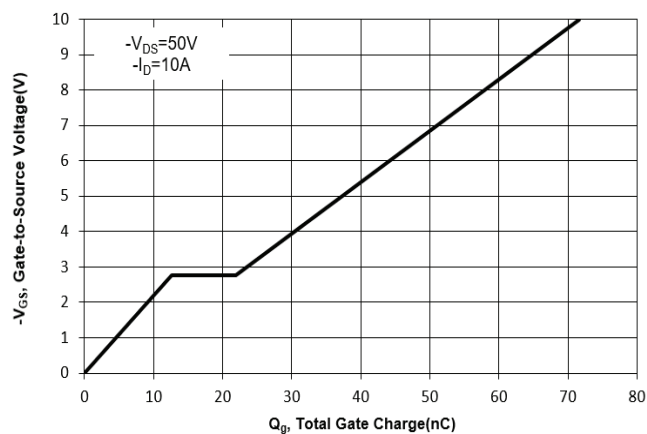


Fig. 12 Safe Operation Area

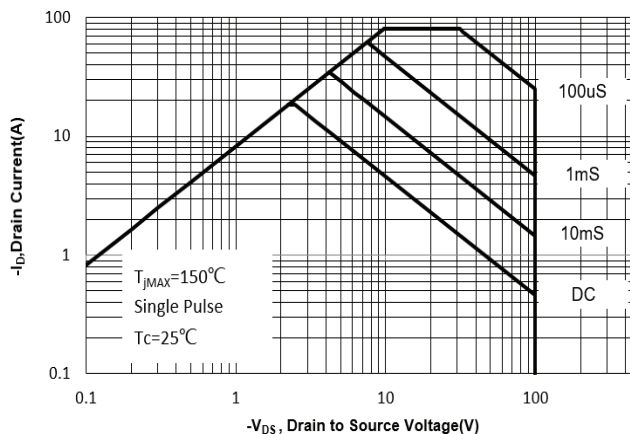


Fig. 13 Normalized Maximum Transient Thermal Impedance($z_{\theta JC}$)

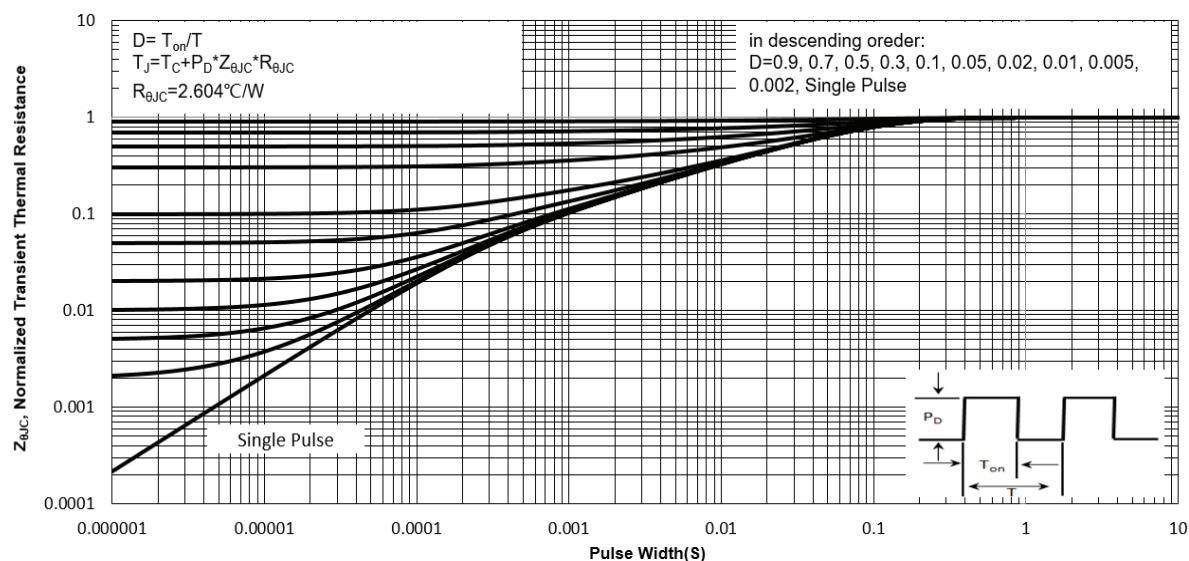
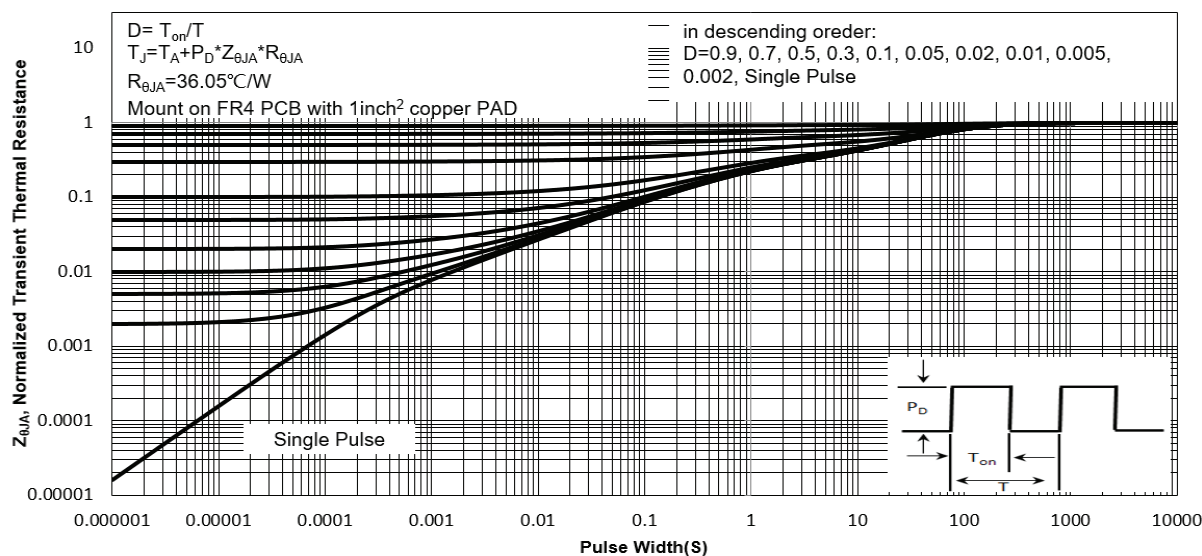


Fig. 14 Normalized Maximum Transient Thermal Impedance($z_{\theta JA}$)



Test Circuits

Fig.1-1 Switching times test circuit

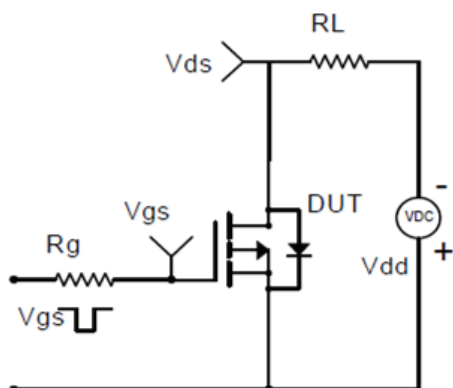


Fig.1-2 Switching Waveform

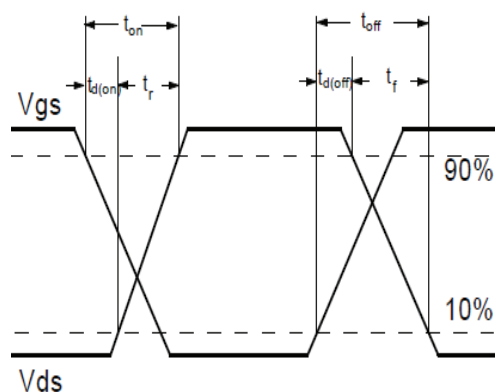


Fig.2-1 Gate charge test circuit

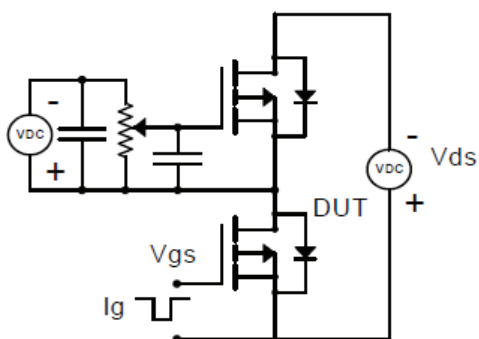


Fig.2-2 Gate charge waveform

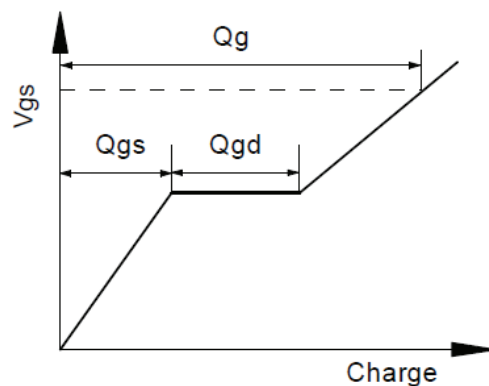


Fig.3-1 Avalanche test circuit

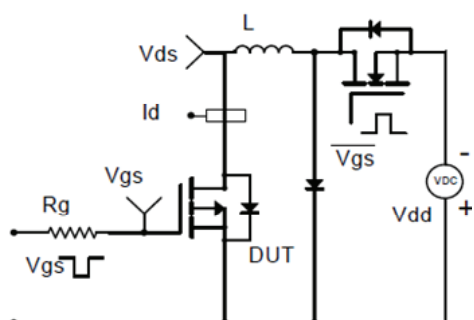
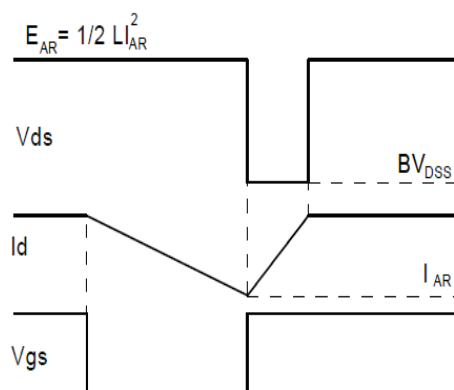
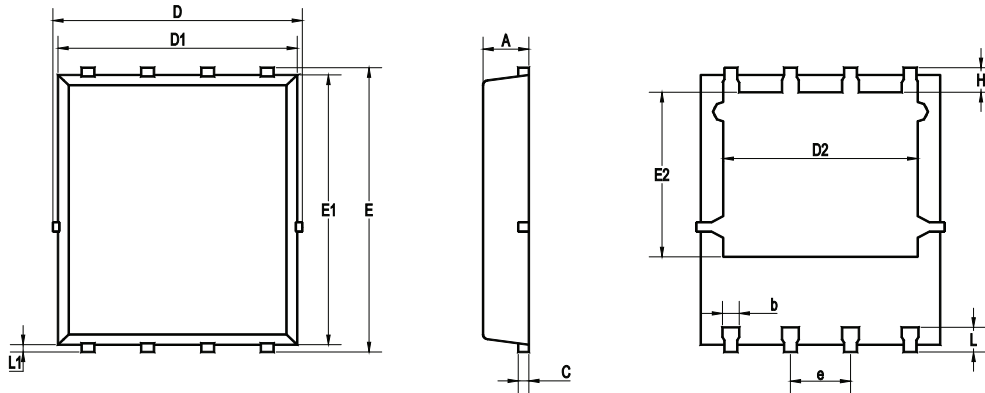


Fig.3-2 Avalanche waveform



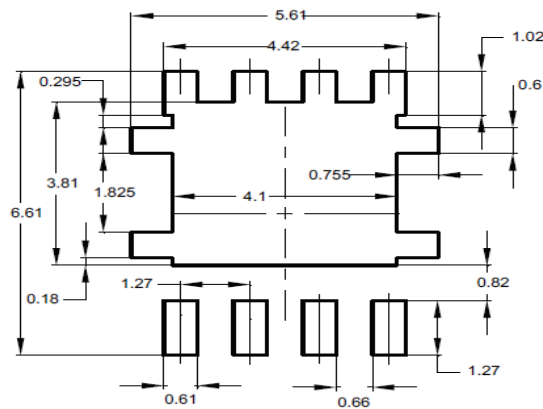
Package Outline Dimensions (Units: mm)

DFN5060



UNIT	A	b	C	D	D1	D2	E	E1	E2	e	L	L1	H
mm	1.12	0.51	0.34	5.26	5.1	4.5	6.25	6	3.66	1.37	0.71	0.2	0.71
	0.9	0.33	0.11	4.7	4.7	3.56	5.75	5.6	3.18	1.17	0.35	0.06	0.35

Recommended Soldering Footprint



Packing information

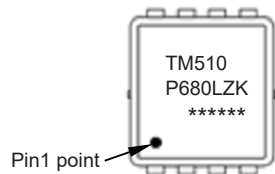
Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN5060	12	8 ± 0.1	0.315 ± 0.004	330	13	5,000

Marking information

" TM510P680LZK " = Part No.

" ***** " = Date Code Marking

Font type: Arial



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