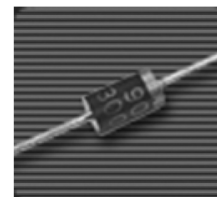


Features

- ◆ Low forward voltage drop
- ◆ High current capability
- ◆ High reliability
- ◆ High surge current capability
- ◆ T_J is 150°C (Max.) and T_{STG} is 175°C (Max.) with PI glue

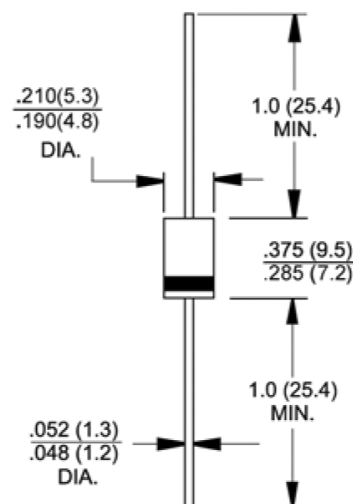
2018



DO-201AD

Mechanical Data

- ◆ Case: Molded plastic DO-201AD
- ◆ Epoxy: UL 94V-O rate flame retardant
- ◆ Lead: Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- ◆ Polarity: Color band denotes cathode end
- ◆ High temperature soldering guaranteed:
250°C/10 seconds .375" (9.5mm) lead
lengths at 5 lbs., (2.3kg) tension
- ◆ Weight: 0.042 ounce, 1.19 grams



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Parameter	Symbols	HER 301	HER 302	HER 303	HER 304	HER 305	HER 306	HER 307	HER 308	Units
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	420	560	700	Volts
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	800	1000	Volts
Maximum average forward rectified current .375" (9.5mm) lead length @T _A =55°C	I _{F(AV)}	3.0								Amps
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	150.0								Amps
Maximum instantaneous forward voltage @ 3.0A	V _F	1.0				1.3	1.7			Volts
Maximum DC reverse current @ T _A =25°C at rated DC blocking voltage @ T _A =100°C	I _R	10.0 200								µA µA
Maximum reverse recovery time (Note 1)	t _{rr}	50					75			nS
Typical junction capacitance (Note 2)	C _J	80					50			pF
Operating junction temperature range	T _J	-55 to +125								°C
Storage temperature range	T _{STG}	-55 to +150								°C

Notes: 1. Reverse Recovery Test Conditions: $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{RR}=0.25\text{A}$
2. Measured at 1 MHz and Applied Reverse Voltage of 4.0 V D.C.

FIG.1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

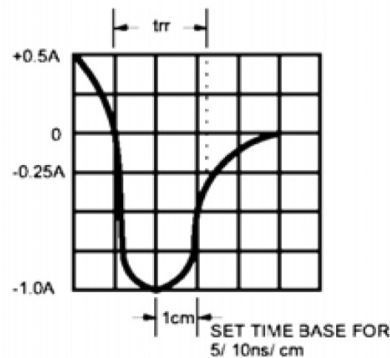
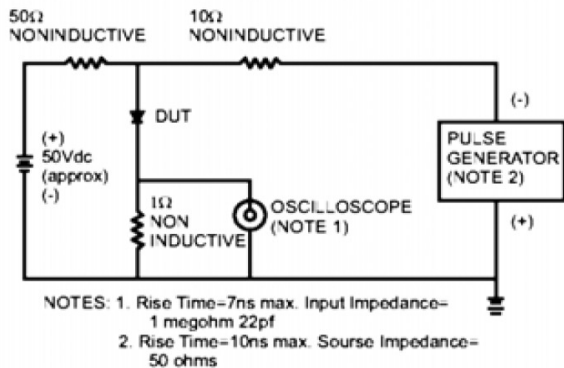


FIG.2- MAXIMUM AVERAGE FORWARD CURRENT DERATING

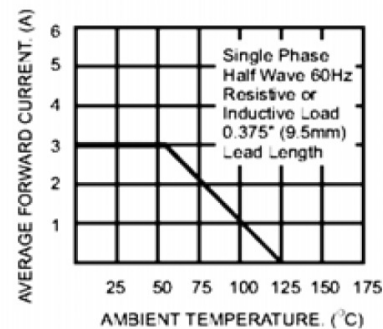


FIG.3- TYPICAL REVERSE CHARACTERISTICS

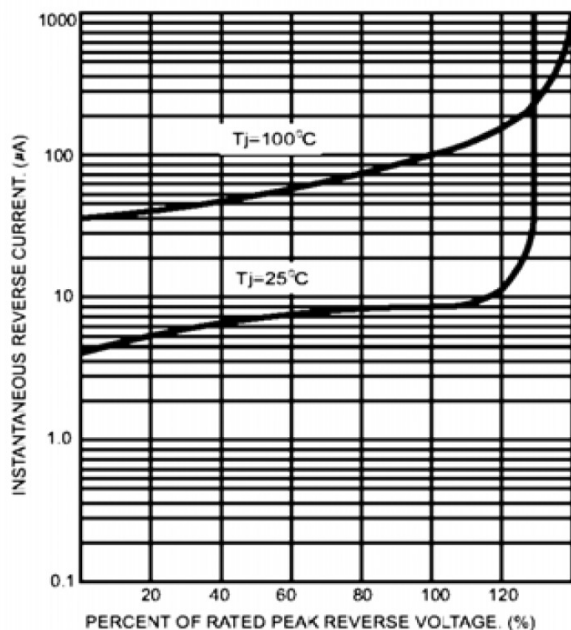


FIG.5- TYPICAL FORWARD CHARACTERISTICS

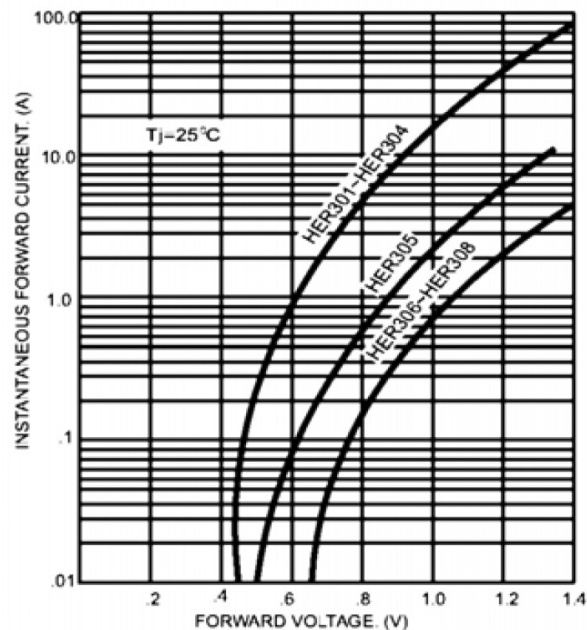


FIG.4- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

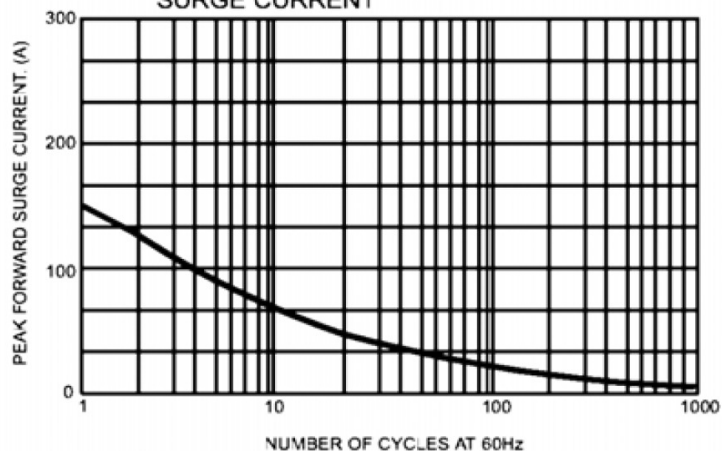


FIG.6- TYPICAL JUNCTION CAPACITANCE

