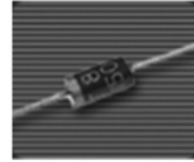


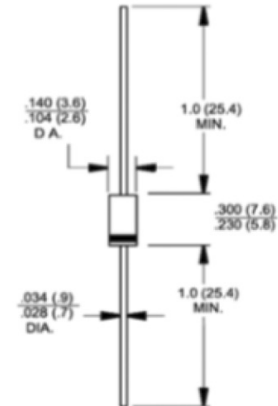
Features

- ◆ Metal-Semiconductor junction with guard ring
- ◆ Epitaxial construction
- ◆ Low forward voltage drop
- ◆ High current capability
- ◆ The plastic material carries UL recognition 94V-0
- ◆ For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

2018



DO-204AC (DO-15)



Dimensions in inches and (millimeters)

Mechanical Data

- ◆ Case : JEDEC DO-204AC/DO-15 molded plastic
- ◆ Polarity : Color band denotes cathode
- ◆ Weight : 0.035 ounce
- ◆ Mounting position : Any

Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Parameter	Symbols	SB370S	SB380S	SB390S	SB3B0S	Units
Maximum repetitive peak reverse voltage	V_{RRM}	70	80	90	100	Volts
Maximum RMS voltage	V_{RMS}	49	56	63	70	Volts
Maximum DC blocking voltage	V_{DC}	70	80	90	100	Volts
Maximum average forward rectified current .375" (9.5mm) lead lengths @ $T_L=100^\circ\text{C}$	I_{AV}	3.0				Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	100.0				Amps
Maximum forward voltage at $I_F=3.0\text{A}, T_J=25^\circ\text{C}$ $I_F=3.0\text{A}, T_J=100^\circ\text{C}$	V_F	0.79 0.69				Volts
Maximum DC reverse current at rated DC blocking voltage @ $T_J=25^\circ\text{C}$ @ $T_J=100^\circ\text{C}$	I_R	0.5 20.0				mA
Typical junction capacitance (Note 1)	C_J	90				pF
Typical thermal resistance (Note 2)	$R_{\theta JL}$	25				$^\circ\text{C/W}$
Operating junction temperature range	T_J	-55 to +150				$^\circ\text{C}$
Storage temperature range	T_{STG}	-55 to +150				$^\circ\text{C}$

Notes: 1. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
2. Thermal Resistance Junction to Lead.

RATINGS AND CHARACTERISTIC CURVES

($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

