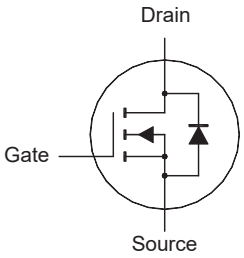
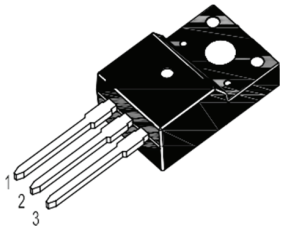
N-Channel Enhancement Mode MOSFET

Features

- High Speed Power Switching
- Logic level driver
- Halogen and Antimony Free(HAF), RoHS compliant





1.Gate 2.Drain 3.Source

TO-220F Plastic Package

Application

- Synchronous Rectification
- Hard Switching and High speed Circuit
- DC-DC in Telecoms and Inductrial

Key Parameters

Parameter	Value	Unit
BV_{DSS}	150	V
$R_{DS(ON)}$ Max	11.7 @ V_{GS} = 10 V	mΩ
	14 @ V_{GS} = 4.5 V	
$V_{GS(th)}$ typ	1.8	V
Q_g typ	66 @ V_{GS} = 10 V	nC

Absolute Maximum Ratings (at T_a = 25℃ unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	T_c = 25℃ T_c = 100℃	I_D	74 46	A
Peak Drain Current, Pulsed [1]		I_{DM}	320	A
Avalanche Current		I_{AS}	27.3	A
Single Pulse Avalanche Energy [2]		E_{AS}	149	mJ
Power Dissipation	T_c = 25℃	P_{tot}	104.1	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to + 150	℃

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.2	℃/W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	45	℃/W

[1] Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.

[2] Limited by $T_{J(MAX)}$, starting T_J = 25°C, L = 0.4 mH, R_g = 25 Ω, I_{AS} = 27.3 A, V_{GS} = 10 V.

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Characteristics at T_a = 25℃ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I_D = 250 µA	BV_{DSS}	150	-	-	V
Drain-Source Leakage Current at V_{DS} = 120 V	I_{DSS}	-	-	1	µA
Gate Leakage Current at V_{GS} = ± 20 V	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage at V_{DS} = V_{GS}, I_D = 250 µA	$V_{GS(th)}$	1.2	-	2.5	V
Drain-Source On-State Resistance at V_{GS} = 10 V, I_D = 20 A at V_{GS} = 4.5 V, I_D = 18 A	$R_{DS(on)}$	- -	9 -	11.7 14	mΩ
DYNAMIC PARAMETERS					
Forward Transconductance at V_{DS} = 5 V, I_D = 20 A	g_{fs}	-	54	-	S
Gate Resistance at V_{GS} = 0 V, V_{DS} = 0 V, f = 1MHz	R_g	-	1.1	-	Ω
Input Capacitance at V_{GS} = 0 V, V_{DS} = 75 V, f = 1 MHz	C_{iss}	-	3823	-	pF
Output Capacitance at V_{GS} = 0 V, V_{DS} = 75 V, f = 1 MHz	C_{oss}	-	349	-	pF
Reverse Transfer Capacitance at V_{GS} = 0 V, V_{DS} = 75 V, f = 1 MHz	C_{rss}	-	27	-	pF
Gate charge total at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 4.5 V	Q_g	- -	66 34	- -	nC
Gate to Source Charge at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V	Q_{gs}	-	12.6	-	nC
Gate to Drain Charge at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V	Q_{gd}	-	15	-	nC
Turn-On Delay Time at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V, R_g = 10 Ω	$t_{d(on)}$	-	53	-	ns
Turn-On Rise Time at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V, R_g = 10 Ω	t_r	-	79	-	ns
Turn-Off Delay Time at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V, R_g = 10 Ω	$t_{d(off)}$	-	41	-	ns
Turn-Off Fall Time at V_{DS} = 75 V, I_D = 20 A, V_{GS} = 10 V, R_g = 10 Ω	t_f	-	36	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at I_S = 20 A, V_{GS} = 0 V	V_{SD}	-	-	1.2	V
Body-Diode Continuous Current	I_S	-	-	74	A
Body-Diode Continuous Current, Pulsed	I_{SM}	-	-	320	A
Body Diode Reverse Recovery Time at I_S = 20 A, di/dt = 100 A / µs	t_{rr}	-	130	-	ns
Body Diode Reverse Recovery Charge at I_S = 20 A, di/dt = 100 A / µs	Q_{rr}	-	349	-	nC

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Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

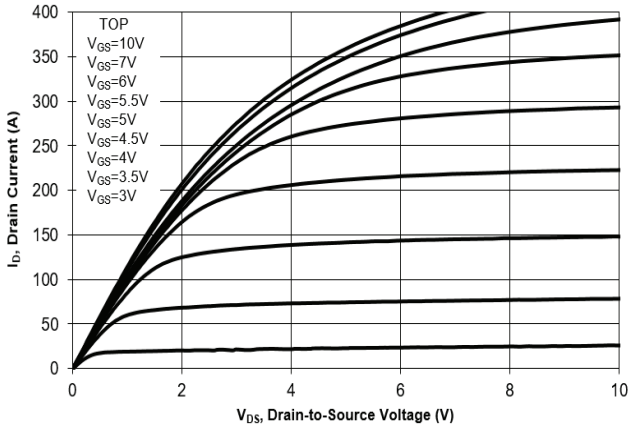


Fig. 2 Typical Transfer Characteristics

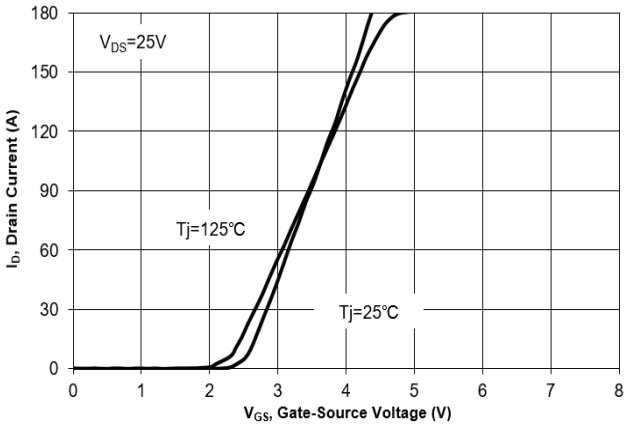


Fig. 3 On-Resistance vs. Drain Current

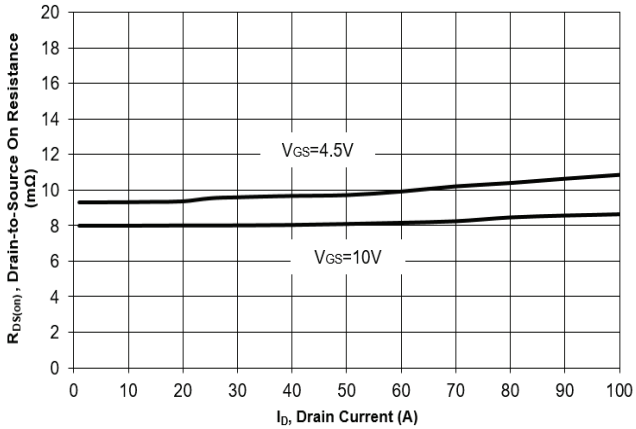


Fig. 4 On-Resistance vs. Gate to Source Voltage

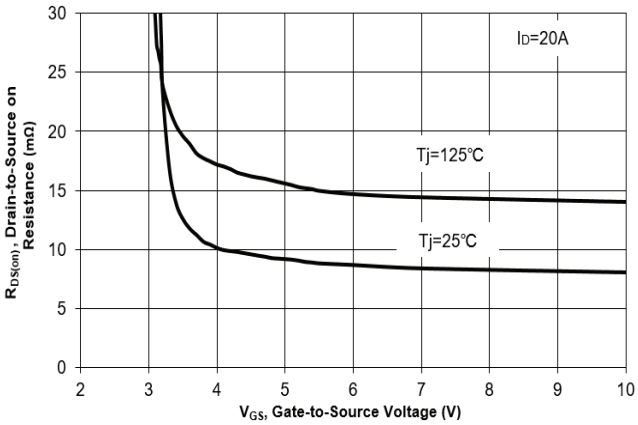


Fig. 5 On-Resistance vs.T_j

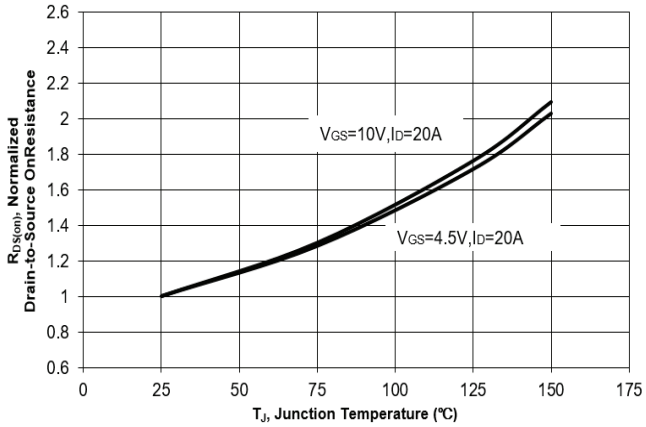
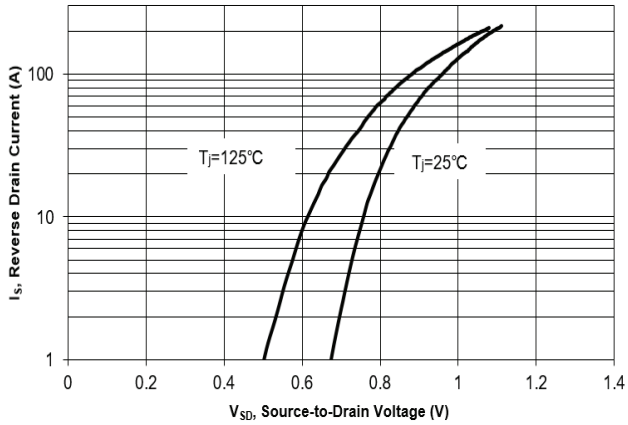


Fig. 6 Typical Body-Diode Forward Characteristics



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Electrical Characteristics Curves

Fig.13 Normalized Maximum Transient Thermal Impedance($z_{\Theta JC}$)

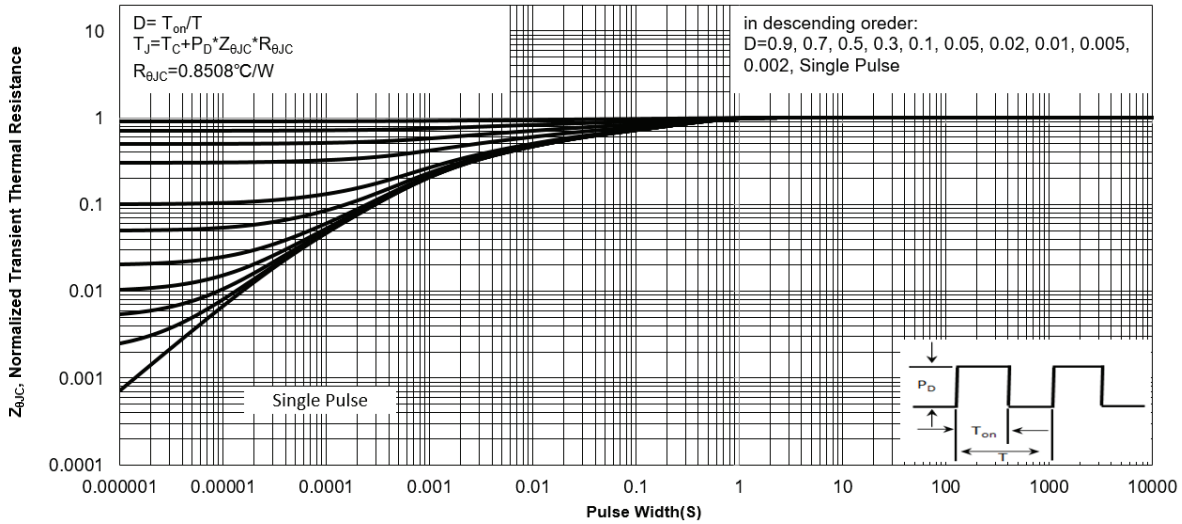
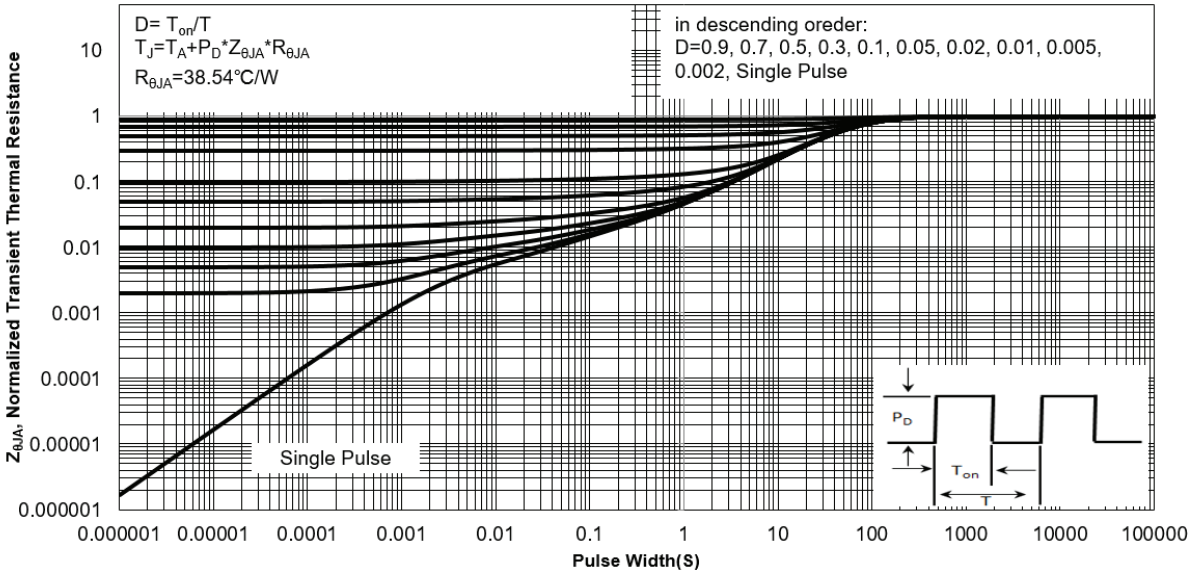


Fig.14 Normalized Maximum Transient Thermal Impedance($z_{\Theta JA}$)



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Test Circuits

Fig.1-1 Switching times test circuit

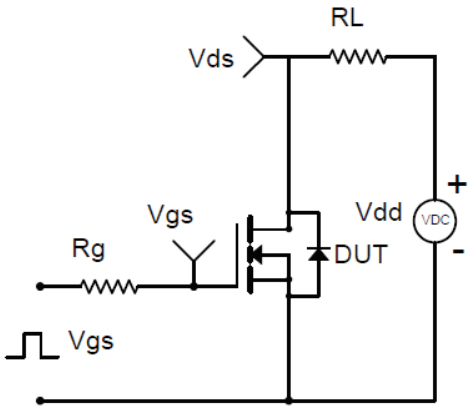


Fig.1-2 Switching Waveform

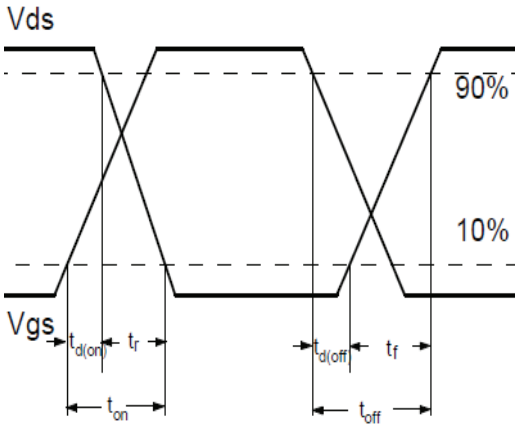


Fig.2-1 Gate charge test circuit

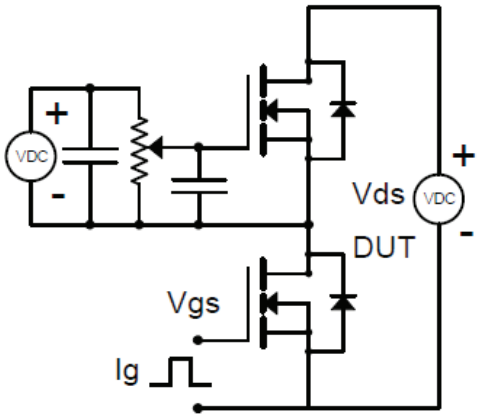


Fig.2-2 Gate charge waveform

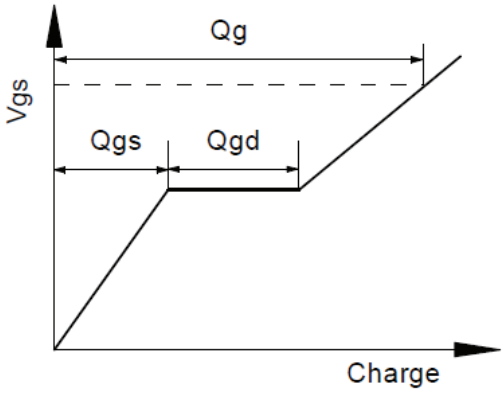


Fig.3-1 Avalanche test circuit

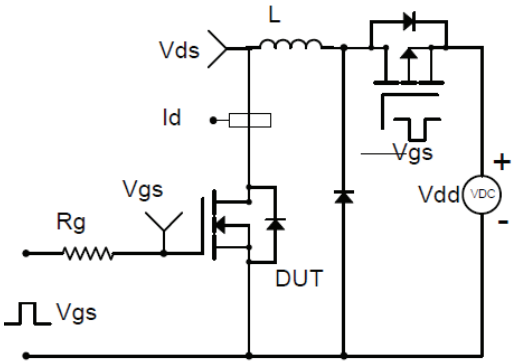
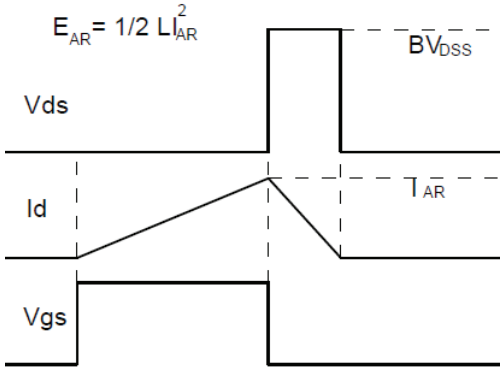


Fig.3-2 Avalanche waveform



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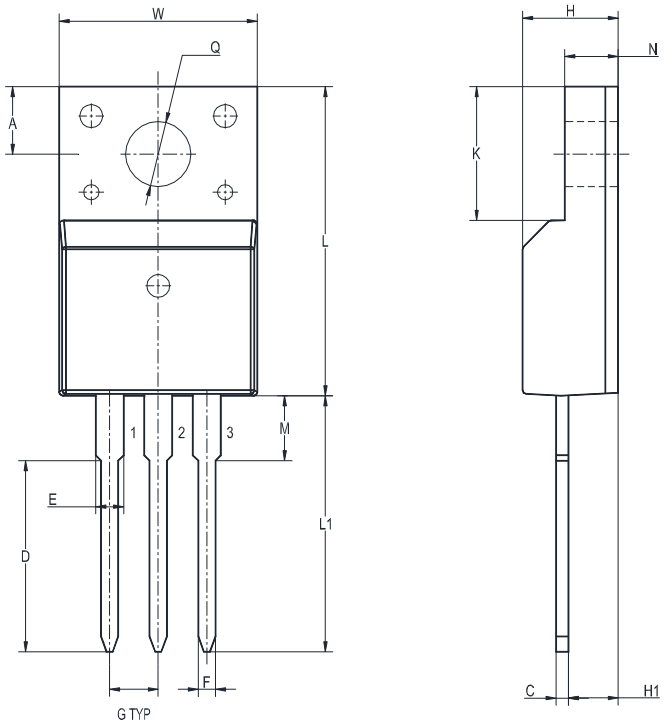
Package Outline Dimensions (Units: mm)

TO-220F



UNIT	A	C	D	E	F	G	W	H	H1	Q	L	L1	M	K	N
mm	3.5	0.7	10.3	1.5	0.9	2.54 TYP.	10.5	4.9	2.9	3.4	16	13.5	3.5	6.7	2.8
	2.8	0.4	9.7	1.1	0.7		9.5	4.5	2.5	2.9	15	12.5	2.9	6.2	2.3

Packing information

Package	Carton Quantity	Box Quantity	Base Quantity	Delivery Mode
TO-220F	5 K / Carton	1 K / Box	50 pcs / Tube	Tube

Marking information

" DAT15N090LS " = Part No.
" ****** " = Date Code Marking
Font type: Arial

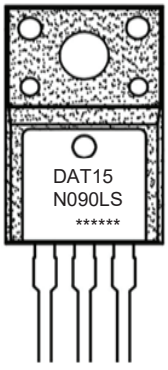


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Fig. 7 Typical Junction Capacitance

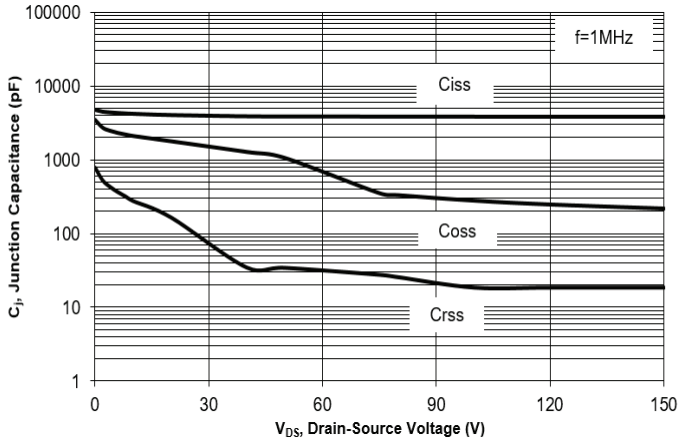


Fig. 8 Drain-Source Leakage Current vs. T_j

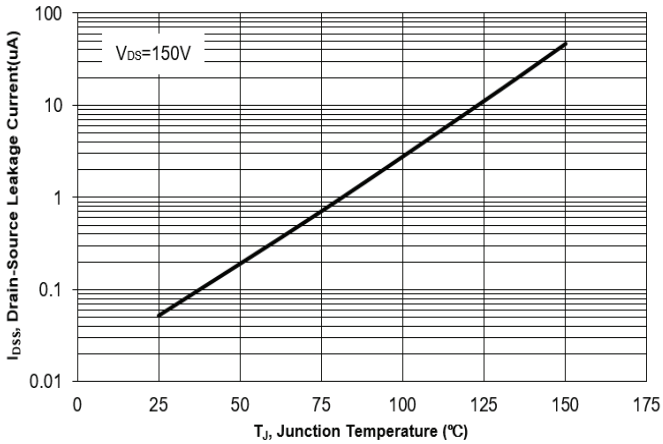


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

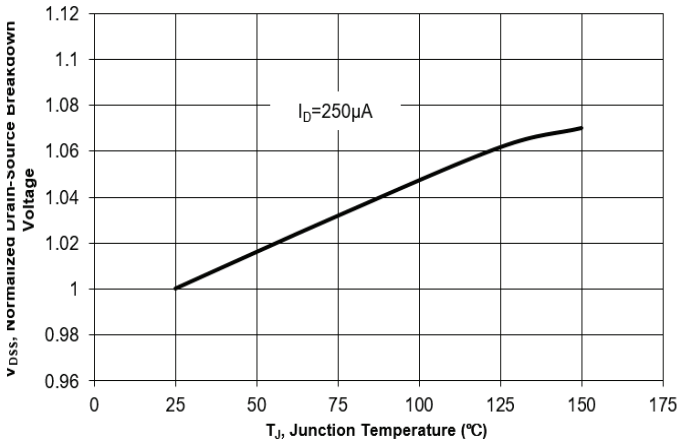


Fig. 10 Gate Threshold Variation vs. T_j

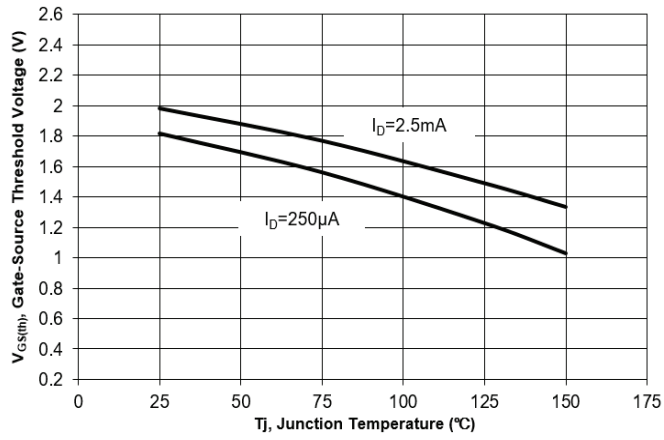


Fig. 11 Gate Charge

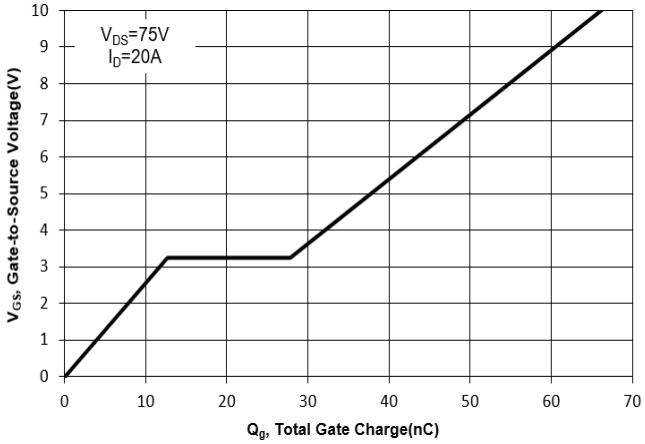
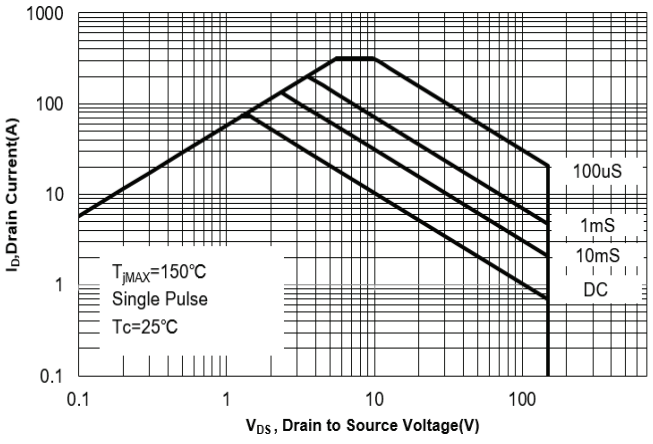


Fig. 12 Safe Operation Area



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