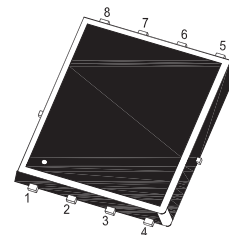
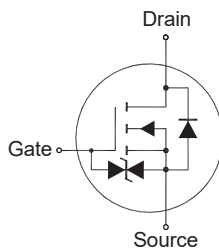


N-Channel Enhancement Mode MOSFET

Features

- Surface-mounted package
- Built-in G-S Protection Diode
- Halogen and Antimony Free(HAF), RoHS compliant
- Typical ESD Protection HBM Class 1C



1.Source 2.Source 3.Source 4.Gate
5.Drain 6.Drain 7.Drain 8.Drain
DFN5060 Plastic Package

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000

Key Parameters

Parameter	Value	Unit
BV_{DSS}	100	V
$R_{DS(ON)}$ Max	68 @ $V_{GS} = 10\text{ V}$	mΩ
	92 @ $V_{GS} = 4.5\text{ V}$	
$V_{GS(th)}$ typ	1.9	V
Q_g typ	9 @ $V_{GS} = 10\text{ V}$	nC

Absolute Maximum Ratings(at $T_a = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	15	A
$T_c = 25^\circ\text{C}$ $T_c = 100^\circ\text{C}$		9.5	
Peak Drain Current, Pulsed ¹⁾	I_{DM}	45	A
Avalanche Current	I_{AS}	4.9	A
Single Pulse Avalanche Energy ²⁾	E_{AS}	6	mJ
Power Dissipation	P_D	20.8	W
$T_c = 25^\circ\text{C}$			
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	6	°C/W
Thermal Resistance from Junction to Ambient ³⁾	$R_{\theta JA}$	58	°C/W

¹⁾ Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.

²⁾ Limited by $T_{J(MAX)}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{ mH}$, $R_g = 25\ \Omega$, $I_{AS} = 4.9\text{ A}$, $V_{GS} = 10\text{ V}$.

³⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

Characteristics at T_a = 25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I _D = 250 μ A	BV _{DSS}	100	-	-	V
Drain-Source Leakage Current at V _{DS} = 80 V	I _{DSS}	-	-	1	μ A
Gate Leakage Current at V _{GS} = $\pm$ 16 V	I _{GSS}	-	-	$\pm$ 10	μ A
Gate-Source Threshold Voltage at V _{DS} = V _{GS} , I _D = 250 μ A	V _{GS(th)}	1.7	-	2.5	V
Drain-Source On-State Resistance at V _{GS} = 10 V, I _D = 10 A at V _{GS} = 4.5 V, I _D = 8 A	R _{DS(on)}	- -	42 -	68 92	m Ω
DYNAMIC PARAMETERS					
Forward Transconductance at V _{DS} = 5 V, I _D = 8 A	g _{fs}	-	9.5	-	S
Gate Resistance at V _{GS} = 0 V, V _{DS} = 0 V, f = 1 MHz	R _g	-	0.7	-	Ω
Input Capacitance at V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	C _{iss}	-	317	-	pF
Output Capacitance at V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	C _{oss}	-	61	-	pF
Reverse Transfer Capacitance at V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	C _{rss}	-	7	-	pF
Gate charge total at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A at V _{DS} = 50 V, V _{GS} = 4.5 V, I _D = 10 A	Q _g	- -	9 4.7	- -	nC
Gate to Source Charge at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A	Q _{gs}	-	1.7	-	nC
Gate to Drain Charge at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A	Q _{gd}	-	3	-	nC
Turn-On Delay Time at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A, R _g = 3.3 Ω	t _{d(on)}	-	6	-	ns
Turn-On Rise Time at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A, R _g = 3.3 Ω	t _r	-	3	-	ns
Turn-Off Delay Time at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A, R _g = 3.3 Ω	t _{d(off)}	-	6	-	ns
Turn-Off Fall Time at V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A, R _g = 3.3 Ω	t _f	-	1.4	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at I _S = 1 A, V _{GS} = 0 V	V _{SD}	-	-	1	V
Body-Diode Continuous Current	I _S	-	-	15	A
Body-Diode Continuous Current, Pulsed	I _{SM}	-	-	45	A
Body Diode Reverse Recovery Time at I _S = 10 A, di/dt = 100 A / μ s	t _{rr}	-	38	-	ns
Body Diode Reverse Recovery Charge at I _S = 10 A, di/dt = 100 A / μ s	Q _{rr}	-	24	-	nC

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

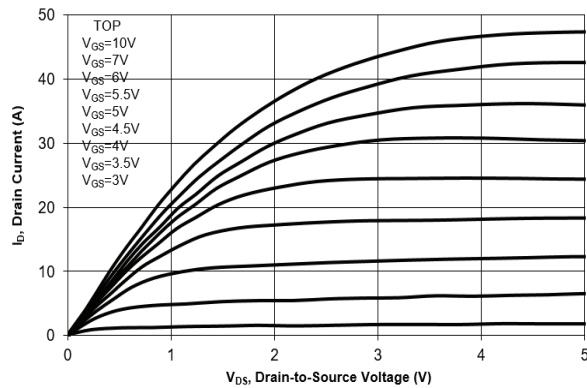


Fig. 2 Typical Transfer Characteristics

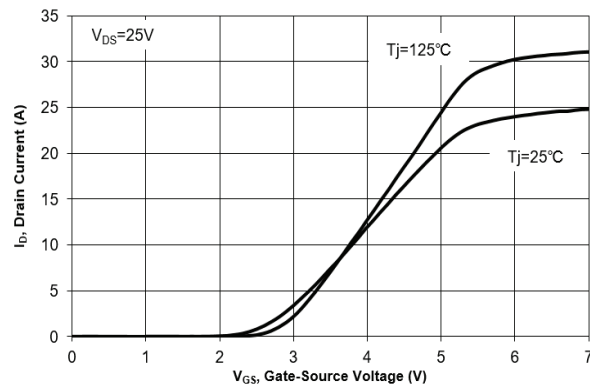


Fig. 3 On-Resistance vs. Drain Current

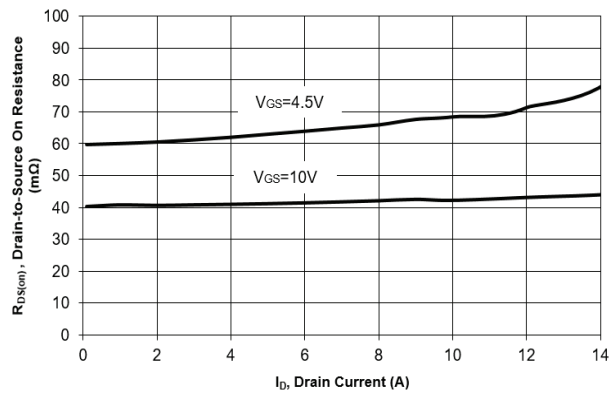


Fig. 4 On-Resistance vs. Gate to Source Voltage

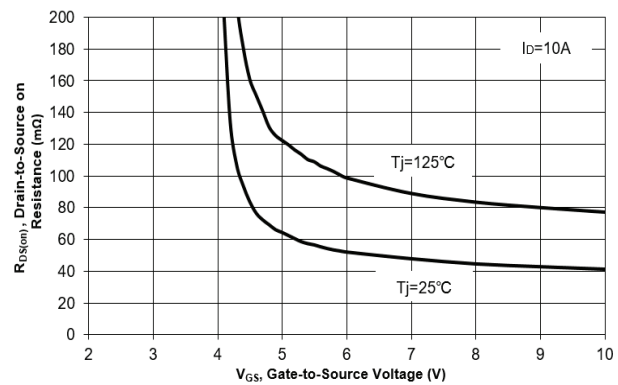


Fig. 5 On-Resistance vs. T_J

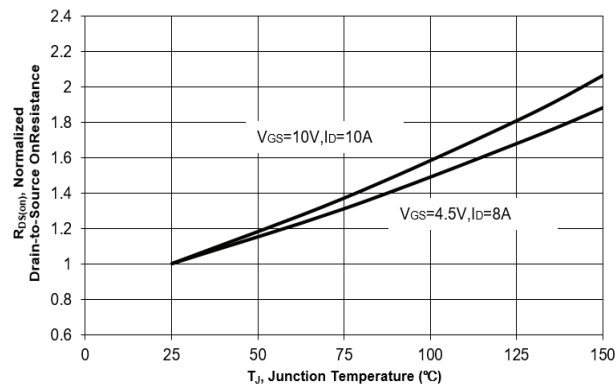
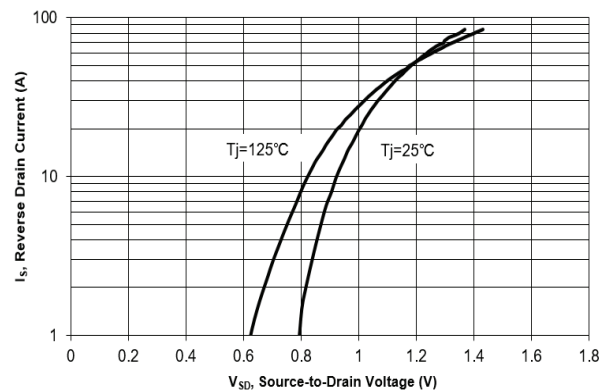


Fig. 6 Typical Body-Diode Forward Characteristics



Electrical Characteristics Curves

Fig.13 Normalized Maximum Transient Thermal Impedance($Z_{\theta JC}$)

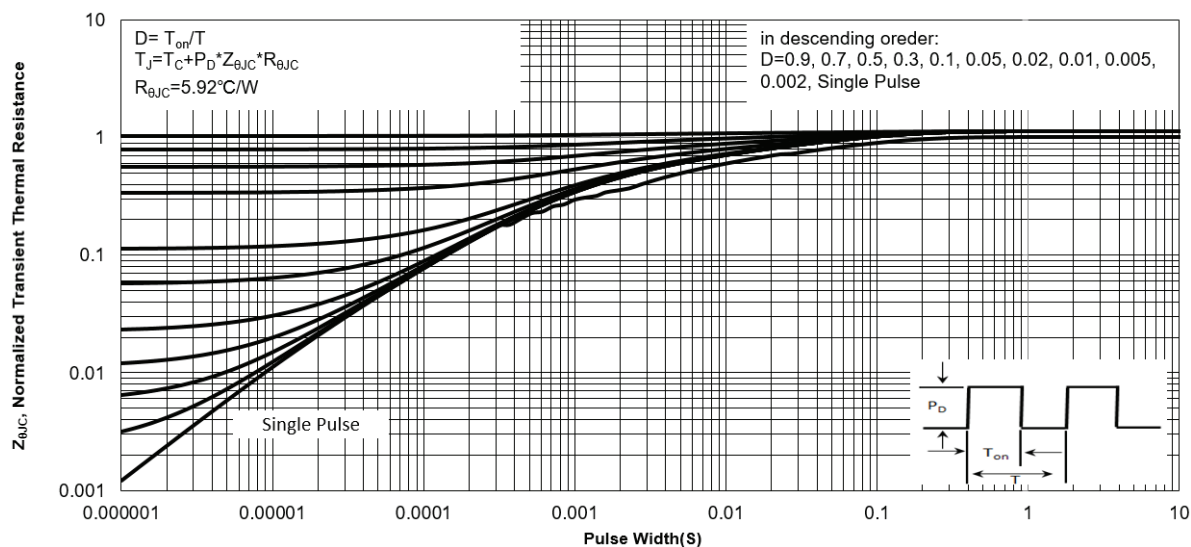


Fig.14 Normalized Maximum Transient Thermal Impedance($Z_{\theta JA}$)

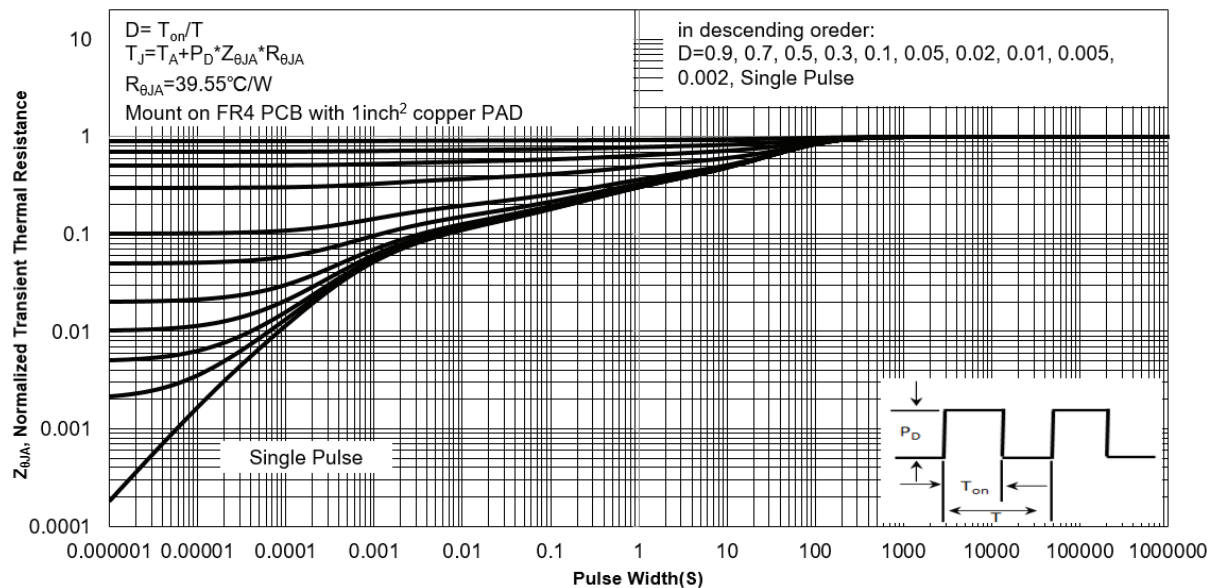


Fig. 7 Typical Junction Capacitance

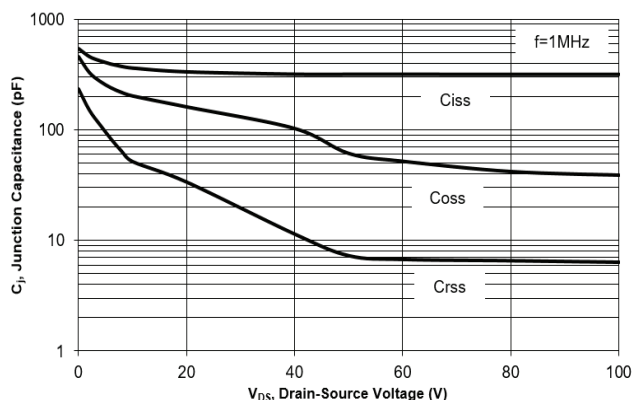


Fig. 8 Drain-Source Leakage Current vs. T_j

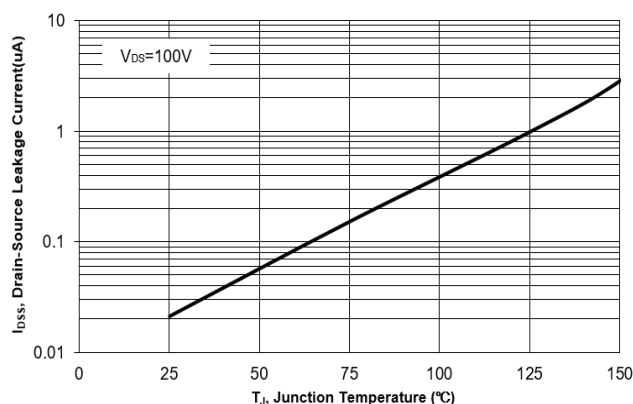


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

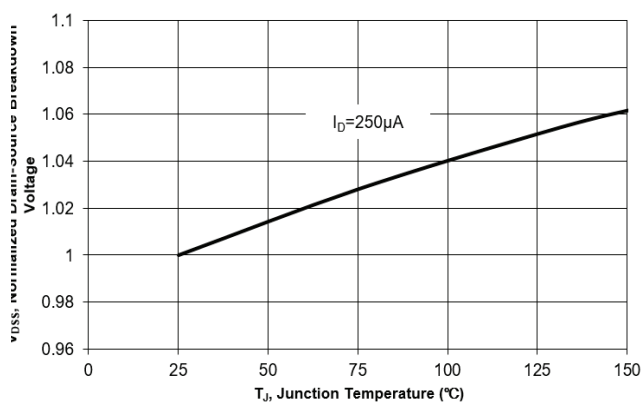


Fig. 10 Gate Threshold Variation vs. T_j

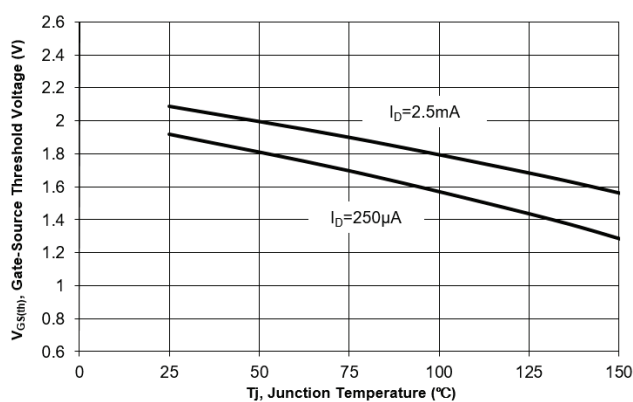


Fig. 11 Gate Charge

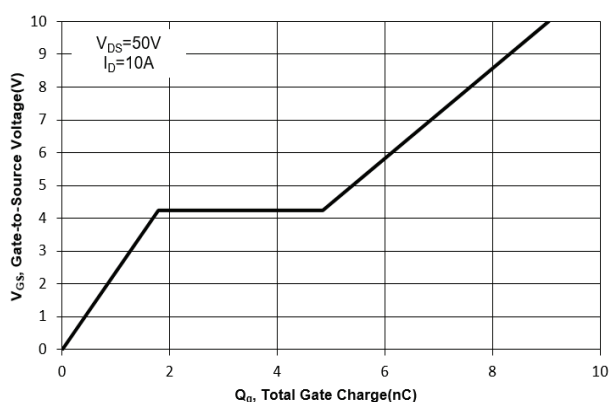
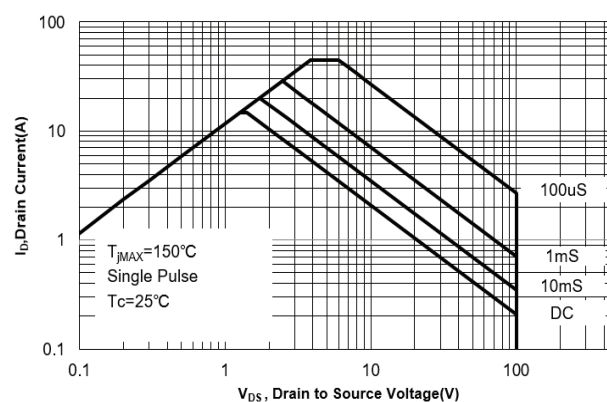


Fig. 12 Safe Operation Area



Test Circuits

Fig.1-1 Switching times test circuit

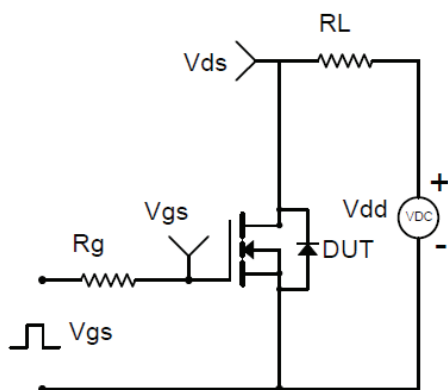


Fig.1-2 Switching Waveform

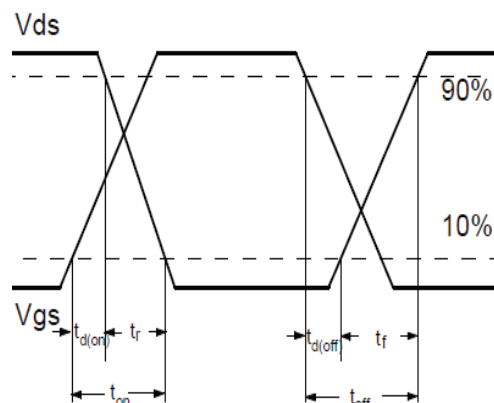


Fig.2-1 Gate charge test circuit

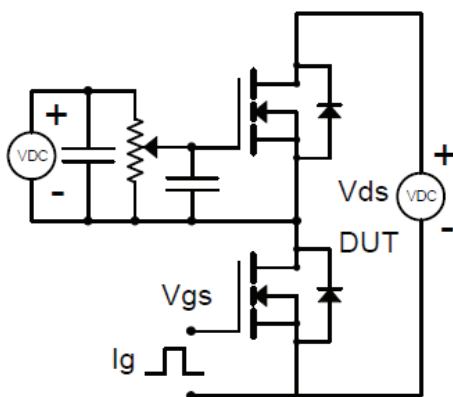


Fig.2-2 Gate charge waveform

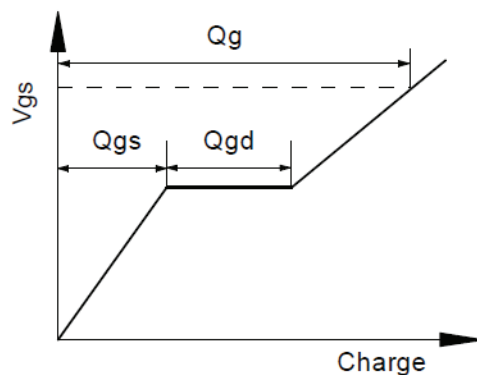


Fig.3-1 Avalanche test circuit

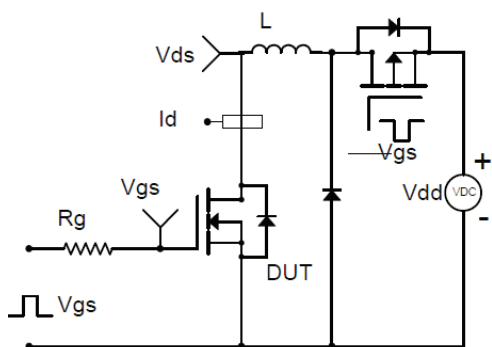
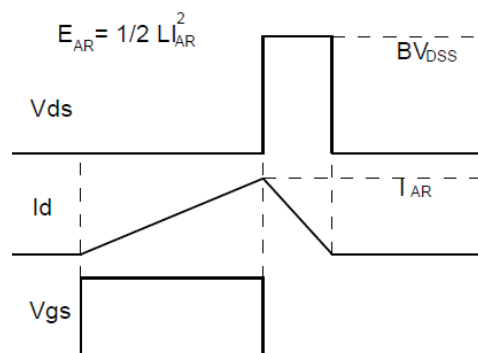
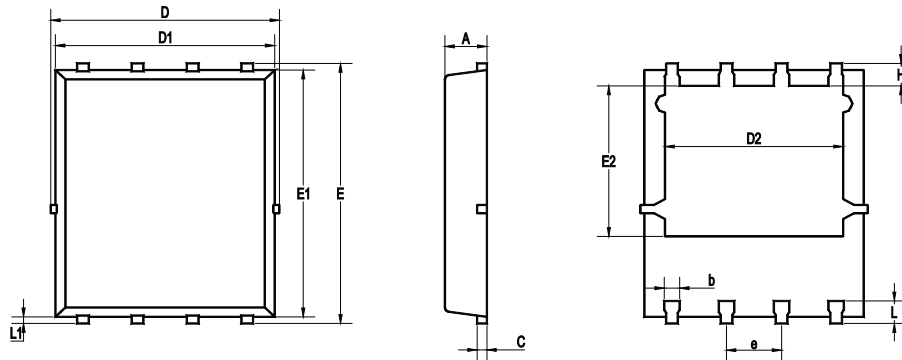


Fig.3-2 Avalanche waveform



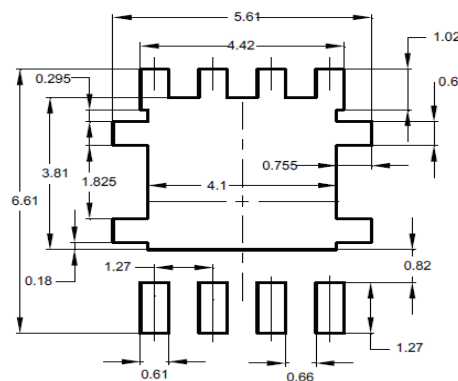
Package Outline Dimensions (Units: mm)

DFN5060



UNIT	A	b	C	D	D1	D2	E	E1	E2	e	L	L1	H
mm	1.12	0.51	0.34	5.26	5.1	4.5	6.25	6	3.66	1.37	0.71	0.2	0.71
	0.9	0.33	0.11	4.7	4.7	3.56	5.75	5.6	3.18	1.17	0.35	0.06	0.35

Recommended Soldering Footprint



Packing information

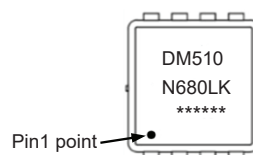
Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN5060	12	8 ± 0.1	0.315 ± 0.004	330	13	5,000

Marking information

" DM510N680LK " = Part No.

" ***** " = Date Code Marking

Font type: Arial



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