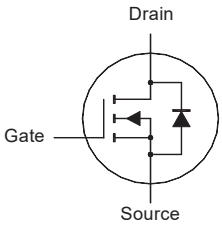
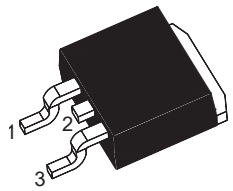
N-Channel Enhancement Mode MOSFET

Features

- Low Gate Charge
- AEC-Q101 Qualified
- Halogen and Antimony Free(HAF), RoHS compliant





1.Gate 2.Drain 3.Source
TO-263 Plastic Package

Applications

- Hard / Soft Switching Topology

Key Parameters

Parameter	Value	Unit
BV_{DSS}	600	V
$R_{DS(ON)}$ Max	280 @ V_{GS} = 10 V	mΩ
$V_{GS(th)}$ typ	3	V
Q_g typ	25.5 @ V_{GS} = 10 V	nC

Absolute Maximum Ratings (at T_a = 25℃ unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current	T_c = 25℃ T_c = 100℃	I_D	12 7.5	A
Peak Drain Current, Pulsed [1]		I_{DM}	38	A
Avalanche Current		I_{AS}	3.5	A
Single Pulsed Avalanche Energy [2]		E_{AS}	484	mJ
Power Dissipation	T_c = 25℃	P_D	113.6	W
Operating Junction and Storage Temperature Range		T_j, T_{stg}	- 55 to + 150	℃

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.1	℃/W
Thermal Resistance from Junction to Ambient [3]	$R_{\theta JA}$	35	℃/W

[1] Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ = 150°C.
[2] Limited by $T_{J(MAX)}$, starting T_J = 25 °C, L = 79 mH, R_g = 25 Ω, I_{AS} = 3.5 A, V_{GS} = 10 V.
[3] Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

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Characteristics at T_a = 25℃ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I_D = 250 µA	BV_{DSS}	600	-	-	V
Drain-Source Leakage Current at V_{DS} = 480 V	I_{DSS}	-	-	1	µA
Gate-Source Leakage Current at V_{GS} = ± 30 V	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage at V_{DS} = V_{GS}, I_D = 250 µA	$V_{GS(th)}$	2	-	4	V
Drain-Source On-State Resistance at V_{GS} = 10 V, I_D = 7 A	$R_{DS(on)}$	-	-	280	mΩ
DYNAMIC PARAMETERS					
Forward Transconductance at V_{DS} = 20 V, I_D = 7 A	g_{fs}	-	7.2	-	S
Gate Resistance at V_{GS} = 0 V, V_{DS} = 0 V, f = 1 MHz	R_g	-	1.5	-	Ω
Input Capacitance at V_{DS} = 300 V, V_{GS} = 0 V, f = 1 MHz	C_{iss}	-	835	-	pF
Output Capacitance at V_{DS} = 300 V, V_{GS} = 0 V, f = 1 MHz	C_{oss}	-	42	-	pF
Reverse Transfer Capacitance at V_{DS} = 300 V, V_{GS} = 0 V, f = 1 MHz	C_{rss}	-	8	-	pF
Total Gate Charge at V_{DS} = 300 V, V_{GS} = 10 V, I_D = 7 A	Q_g	-	25.5	-	nC
Gate Source Charge at V_{DS} = 300 V, V_{GS} = 10 V, I_D = 7 A	Q_{gs}	-	5.2	-	nC
Gate Drain Charge at V_{DS} = 300 V, V_{GS} = 10 V, I_D = 7 A	Q_{gd}	-	11.5	-	nC
Turn-On Delay Time at V_{DD} = 300 V, V_{GS} = 10 V, I_D = 7 A, R_G = 24 Ω	$t_{d(on)}$	-	36	-	ns
Turn-On Rise Time at V_{DD} = 300 V, V_{GS} = 10 V, I_D = 7 A, R_G = 24 Ω	t_r	-	12	-	ns
Turn-Off Delay Time at V_{DD} = 300 V, V_{GS} = 10 V, I_D = 7 A, R_G = 24 Ω	$t_{d(off)}$	-	34	-	ns
Turn-Off Fall Time at V_{DD} = 300 V, V_{GS} = 10 V, I_D = 7 A, R_G = 24 Ω	t_f	-	28	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at I_S = 1 A, V_{GS} = 0 V	V_{SD}	-	-	1.4	V
Body-Diode Continuous Current	I_S	-	-	12	A
Body-Diode Continuous Current, Pulsed	I_{SM}	-	-	38	A
Body Diode Reverse Recovery Time at I_S = 7 A, di/dt = 100 A / µs	t_{rr}	-	315	-	ns
Body Diode Reverse Recovery Charge at I_S = 7 A, di/dt = 100 A / µs	Q_{rr}	-	3.2	-	µC

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Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

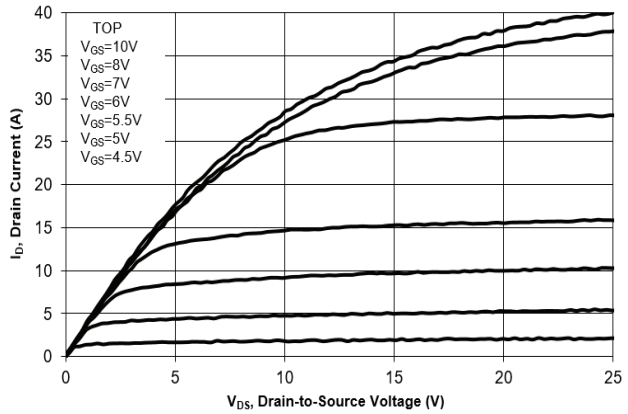


Fig. 2 Typical Transfer Characteristics

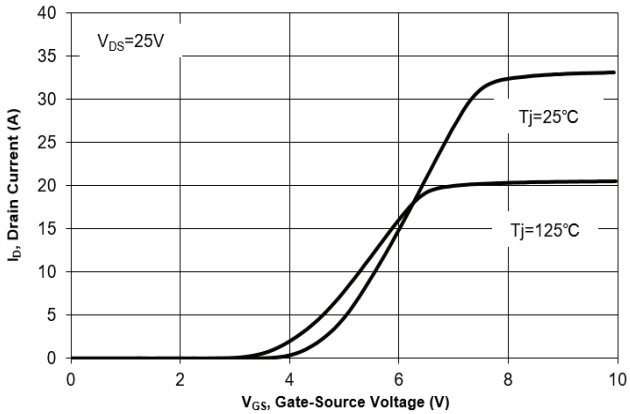


Fig. 3 On-Resistance vs. Drain Current

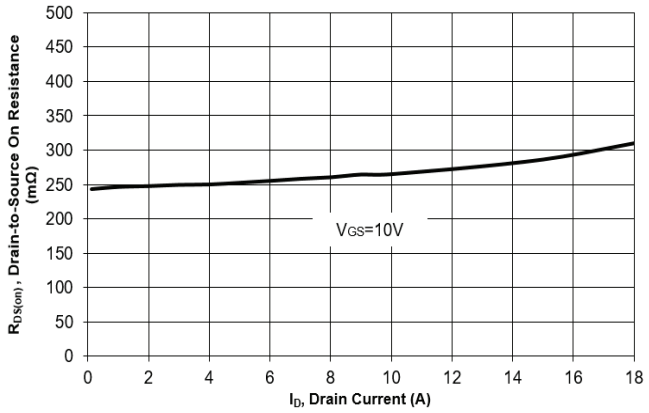


Fig. 4 On-Resistance vs. Gate to Source Voltage

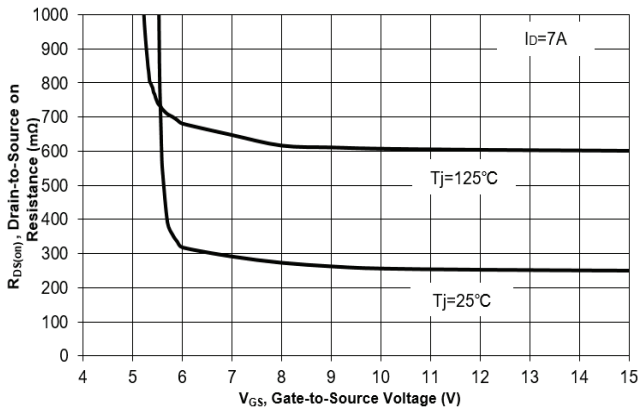


Fig. 5 On-Resistance vs.T_j

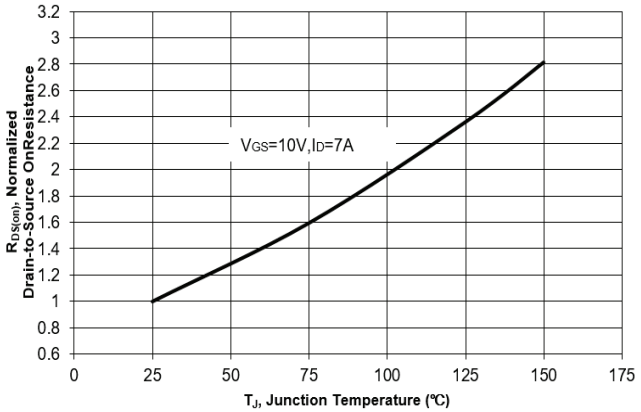
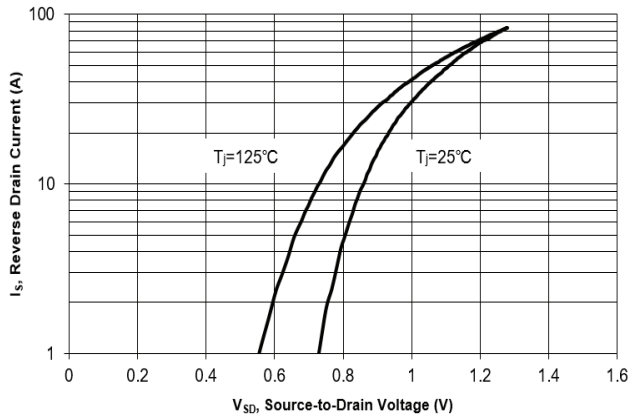


Fig. 6 Typical Body-Diode Forward Characteristics



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Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

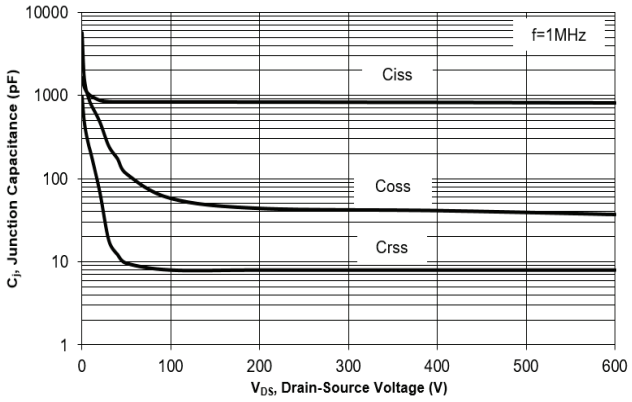

Fig. 8 Drain-Source Leakage Current vs. T_j

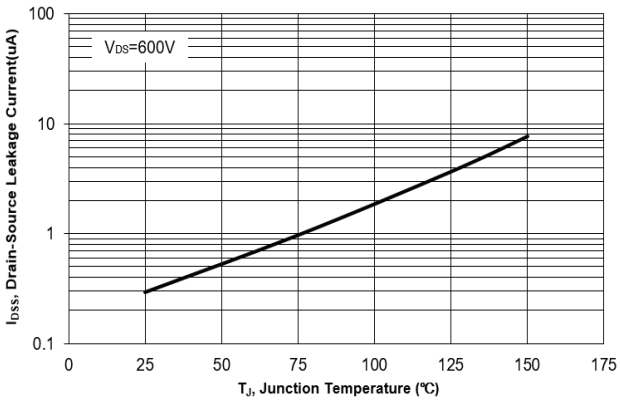

Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

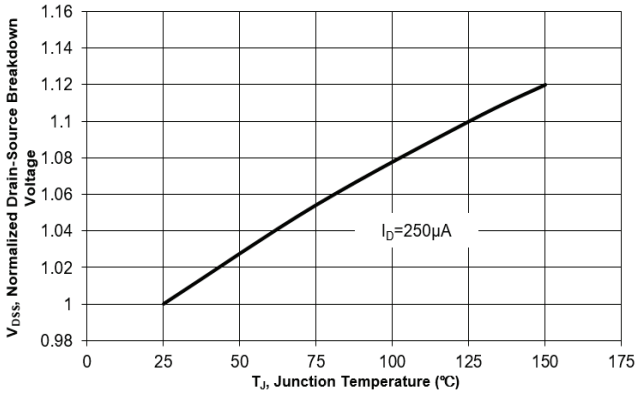

Fig. 10 Gate Threshold Variation vs. T_j

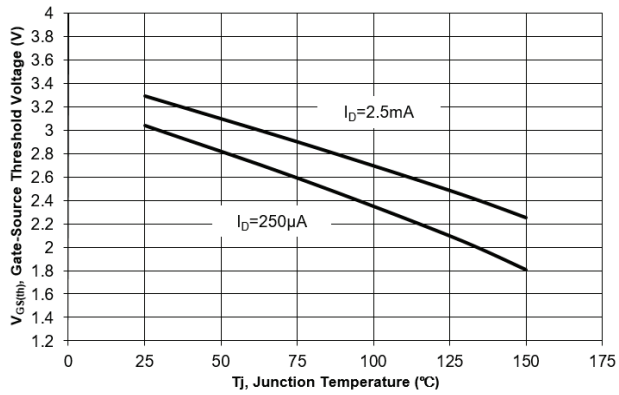

Fig. 11 Gate Charge

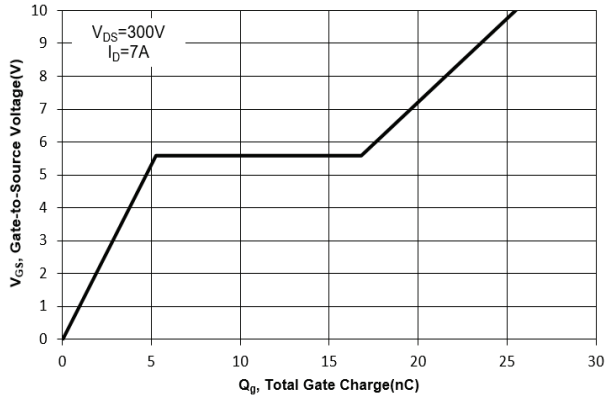

Fig. 12 Safe Operation Area

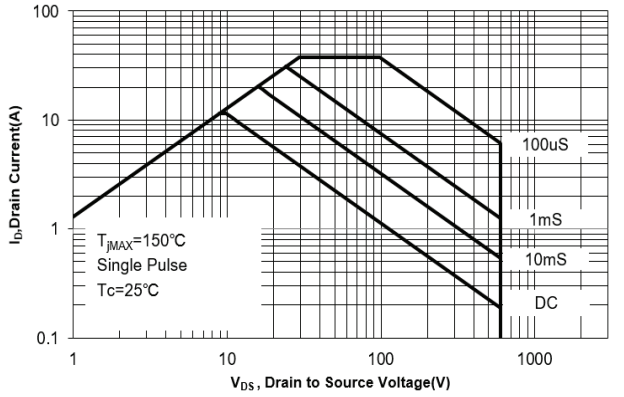

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Fig. 13 Normalized Maximum Transient Thermal Impedance($Z_{\Theta JC}$)

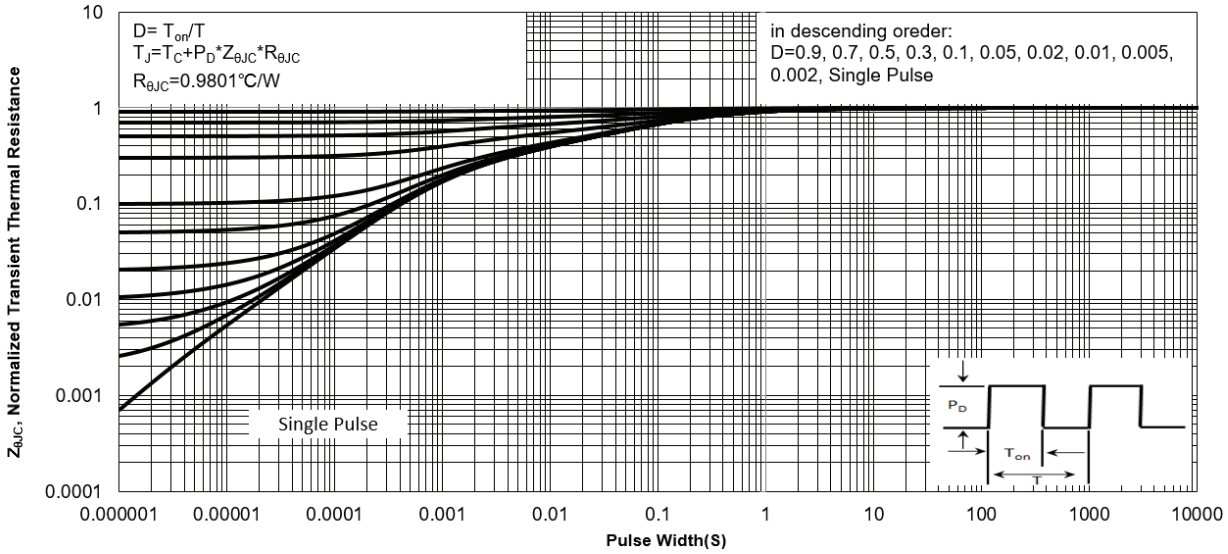
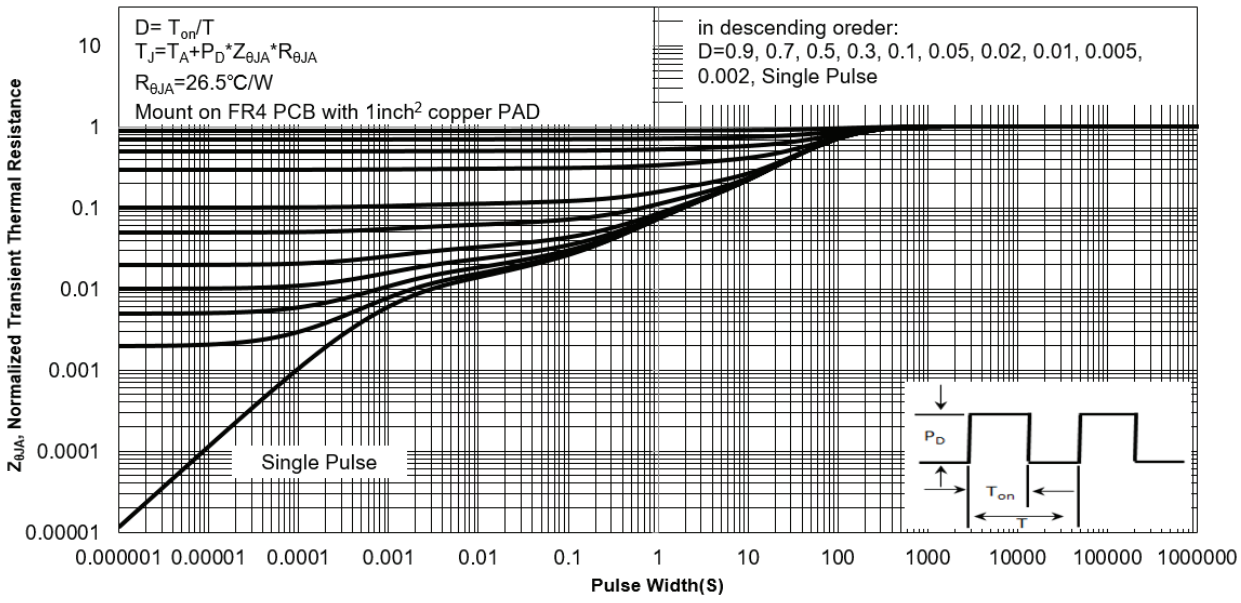


Fig. 14 Normalized Maximum Transient Thermal Impedance($Z_{\Theta JA}$)



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Test Circuits

Fig.1-1 Switching times test circuit

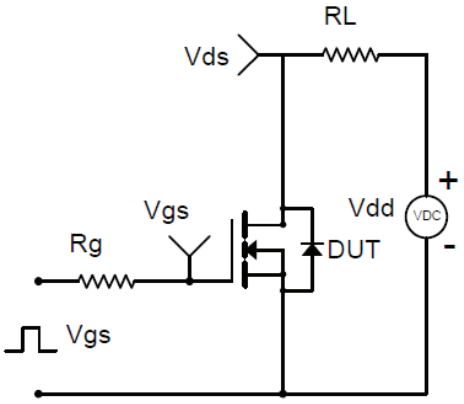


Fig.1-2 Switching Waveform

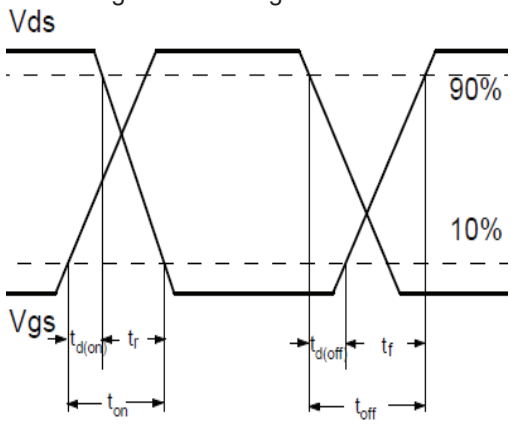


Fig.2-1 Gate charge test circuit

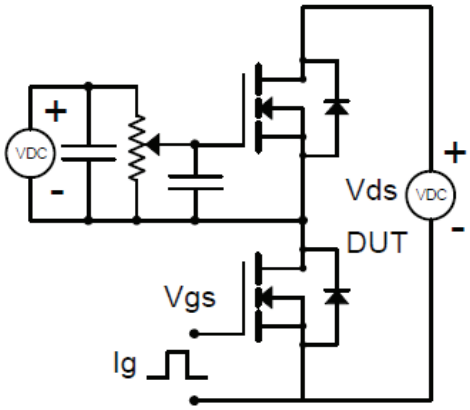


Fig.2-2 Gate charge waveform

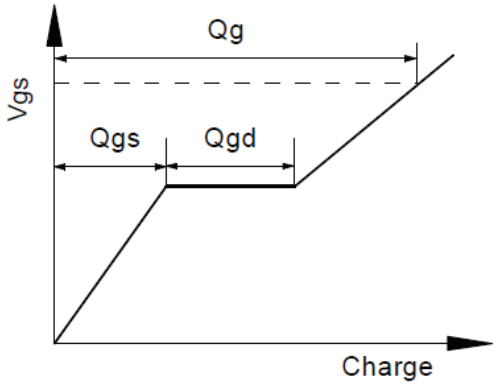


Fig.3-1 Avalanche test circuit

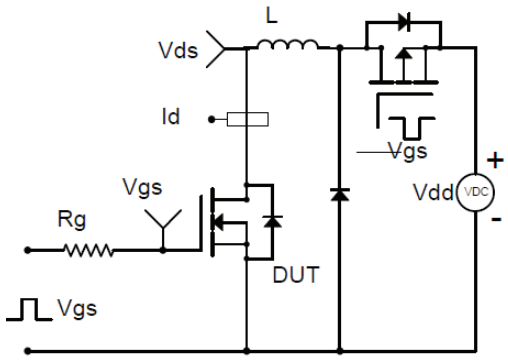
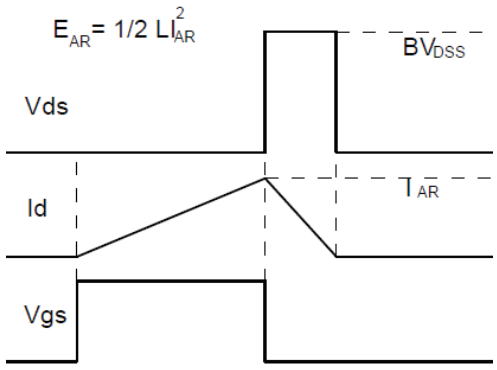


Fig.3-2 Avalanche waveform



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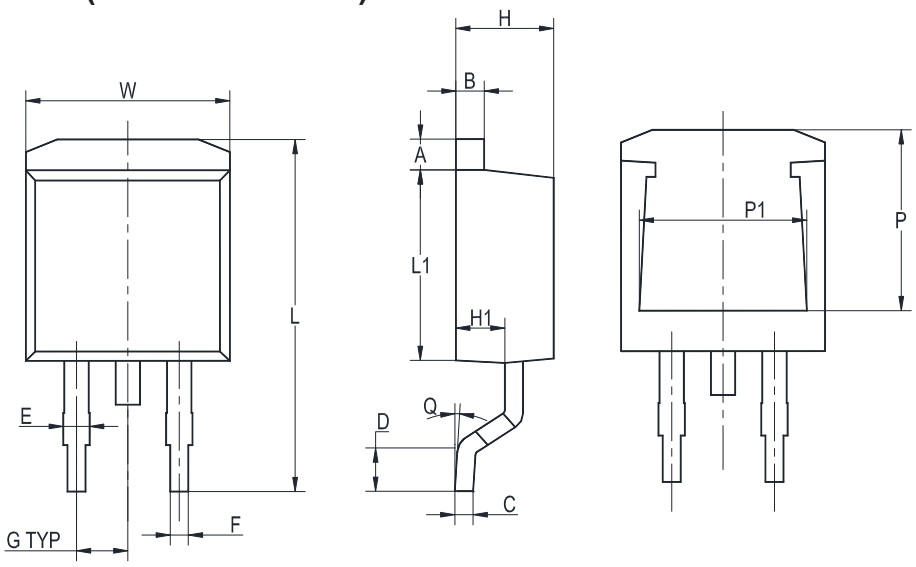
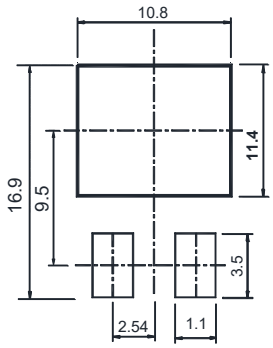
Package Outline (Dimensions in mm)

TO-263



UNIT	A	B	C	D	E	F	G	W	H	H1	L	L1	Q	P	P1
mm	1.5	1.5	0.5	2.60	1.6	0.94	2.54 TYP	10.5	4.8	2.9	16.5	8.7	8° MAX	7.6	8.2
	1.1	1.1	0.3	2.15	1.1	0.68		9.6	4.4	2.5	14.5	8.2		7.1	7.4

Recommended Soldering Footprint



Packing information

Package	Reel Quantity	Box Quantity	Carton Quantity	Delivery Mode
TO-263	0.8 K / Reel	0.8 K / Box	4K pcs / Carton	Reel

Marking information

" PV60N280 " = Part No.
" ****** " = Date Code Marking
Font type: Arial

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