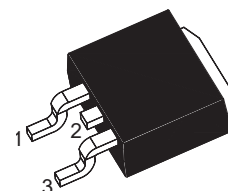
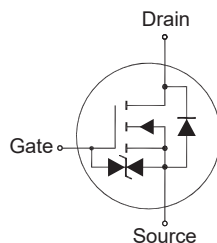


N-Channel Enhancement Mode MOSFET

Features

- AEC-Q101 Qualified
- Surface-mounted package
- Built-in G-S Protection Diode
- Halogen and Antimony Free(HAF), RoHS compliant
- Typical ESD Protection HBM Class 2



1. Gate 2. Drain 3. Source
TO-263 Plastic Package

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000

Key Parameters

Parameter	Value	Unit
BV_{DSS}	650	V
$R_{DS(ON)} \text{ Max}$	430 @ $V_{GS} = 10 \text{ V}$	mΩ
$V_{GS(th)} \text{ typ}$	2.9	V
$Q_g \text{ typ}$	18.4 @ $V_{GS} = 10 \text{ V}$	nC

Absolute Maximum Ratings(at $T_a = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	V
Drain Current	I_D	7.9 4.9	A
		$T_c = 25^\circ\text{C}$ $T_c = 100^\circ\text{C}$	
Peak Drain Current, Pulsed ¹⁾	I_{DM}	25	A
Avalanche Current	I_{AS}	2.1	A
Single Pulse Avalanche Energy ²⁾	E_{AS}	174	mJ
Power Dissipation	P_D	62.5	W
		$T_c = 25^\circ\text{C}$	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2	°C/W
Thermal Resistance from Junction to Ambient ³⁾	$R_{\theta JA}$	40	°C/W

¹⁾ Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$.

²⁾ Limited by $T_{J(MAX)}$, starting $T_J = 25^\circ\text{C}$, $L = 79 \text{ mH}$, $R_g = 25 \Omega$, $I_{AS} = 2.1 \text{ A}$, $V_{GS} = 10 \text{ V}$.

³⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $I_D = 250\ \mu\text{A}$	BV_{DSS}	650	-	-	V
Drain-Source Leakage Current at $V_{DS} = 520\ \text{V}$	I_{DSS}	-	-	1	μA
Gate Leakage Current at $V_{GS} = \pm 24\ \text{V}$	I_{GSS}	-	-	± 1	μA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	$V_{GS(th)}$	2	-	4	V
Drain-Source On-State Resistance at $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$	$R_{DS(on)}$	-	330	430	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Forward Transconductance at $V_{DS} = 5\ \text{V}$, $I_D = 4\ \text{A}$	g_{FS}	-	8.6	-	S
Gate Resistance at $V_{GS} = 0\ \text{V}$, $V_{DS} = 0\ \text{V}$, $f = 1\ \text{MHz}$	R_g	-	12.3	-	Ω
Input Capacitance at $V_{GS} = 0\ \text{V}$, $V_{DS} = 325\ \text{V}$, $f = 1\ \text{MHz}$	C_{iss}	-	722	-	pF
Output Capacitance at $V_{GS} = 0\ \text{V}$, $V_{DS} = 325\ \text{V}$, $f = 1\ \text{MHz}$	C_{oss}	-	24	-	pF
Reverse Transfer Capacitance at $V_{GS} = 0\ \text{V}$, $V_{DS} = 325\ \text{V}$, $f = 1\ \text{MHz}$	C_{rss}	-	5.9	-	pF
Gate charge total at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$	Q_g	-	18.4	-	nC
Gate to Source Charge at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$	Q_{gs}	-	4	-	nC
Gate to Drain Charge at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$	Q_{gd}	-	6.5	-	nC
Turn-On Delay Time at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$, $R_G = 24\ \Omega$	$t_{d(on)}$	-	43	-	ns
Turn-On Rise Time at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$, $R_G = 24\ \Omega$	t_r	-	13	-	ns
Turn-Off Delay Time at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$, $R_G = 24\ \Omega$	$t_{d(off)}$	-	43	-	ns
Turn-Off Fall Time at $V_{DS} = 325\ \text{V}$, $V_{GS} = 10\ \text{V}$, $I_D = 4\ \text{A}$, $R_G = 24\ \Omega$	t_f	-	50	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $I_S = 1\ \text{A}$, $V_{GS} = 0\ \text{V}$	V_{SD}	-	-	1.4	V
Body-Diode Continuous Current	I_S	-	-	7.9	A
Body-Diode Continuous Current, Pulsed	I_{SM}	-	-	25	A
Body Diode Reverse Recovery Time at $I_S = 4\ \text{A}$, $di/dt = 100\ \text{A}/\mu\text{s}$	t_{rr}	-	251	-	ns
Body Diode Reverse Recovery Charge at $I_S = 4\ \text{A}$, $di/dt = 100\ \text{A}/\mu\text{s}$	Q_{rr}	-	2	-	μC

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

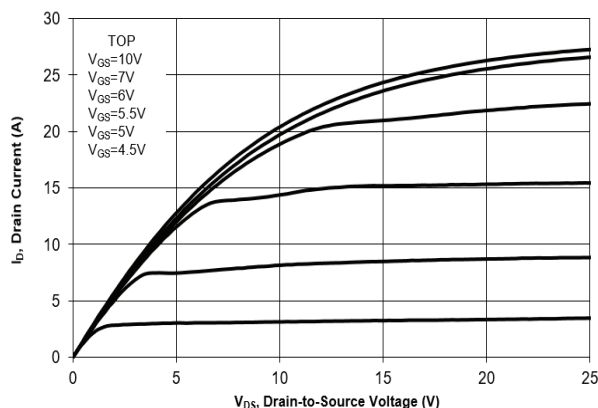


Fig. 2 Typical Transfer Characteristics

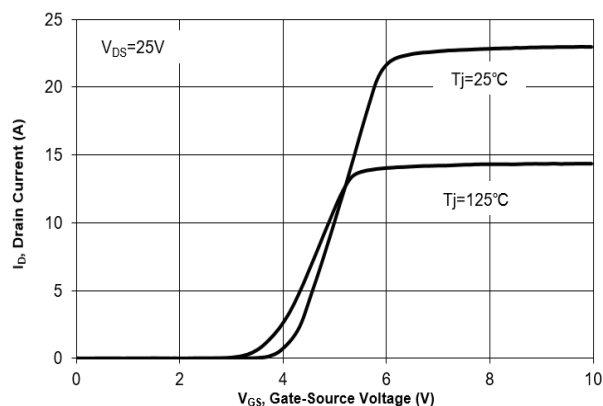


Fig. 3 on-Resistance vs Drain Current

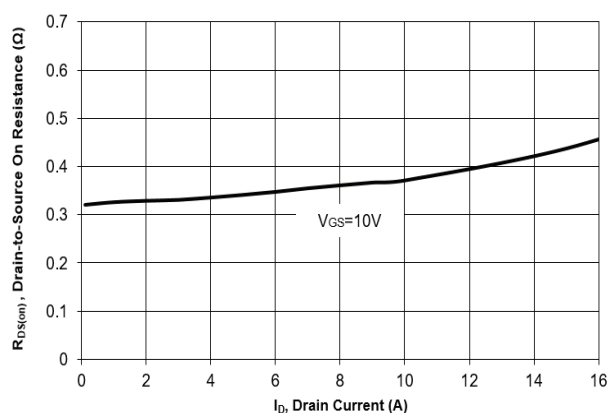


Fig. 4 on-Resistance vs. Gate to Source Voltage

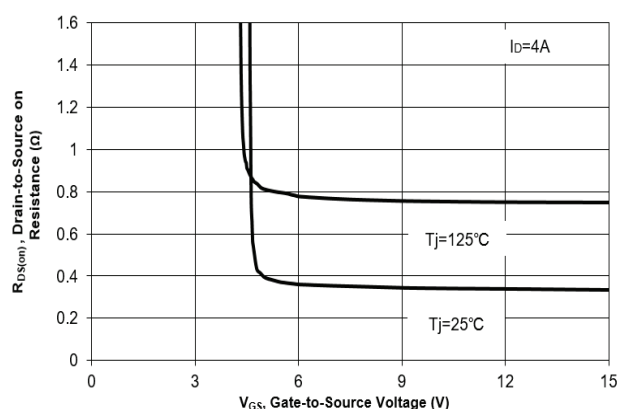


Fig. 5 on-Resistance vs. T_j

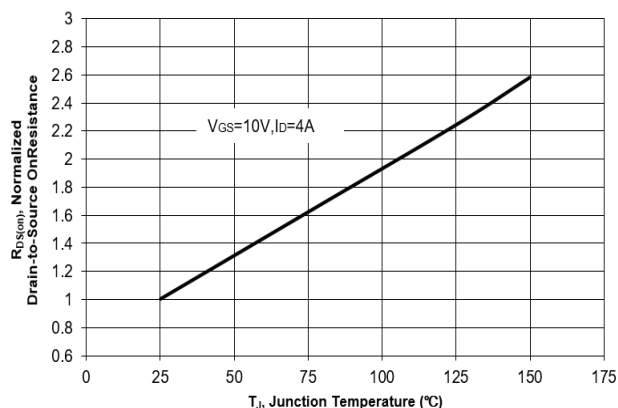
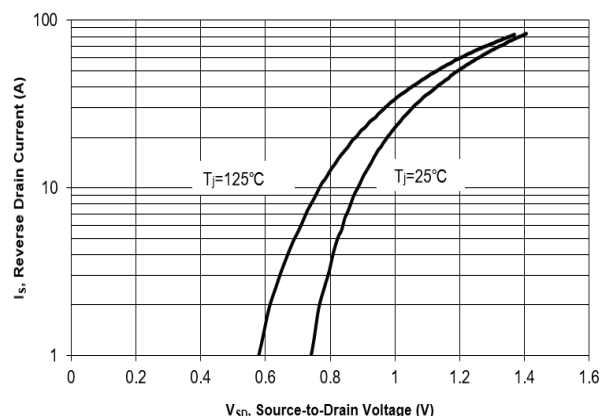


Fig. 6 Typical Body Diode Forward Characteristics



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

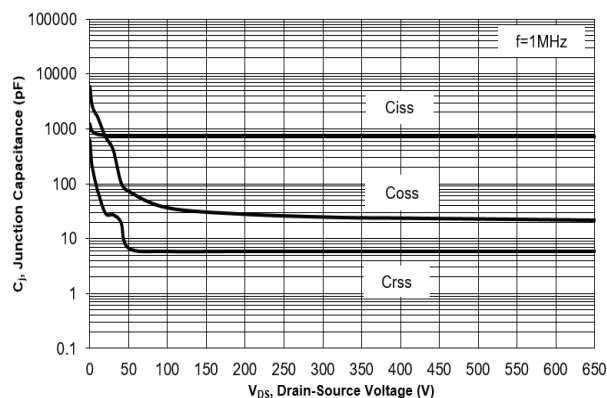


Fig. 8 Drain-Source Leakage Current vs. T_J

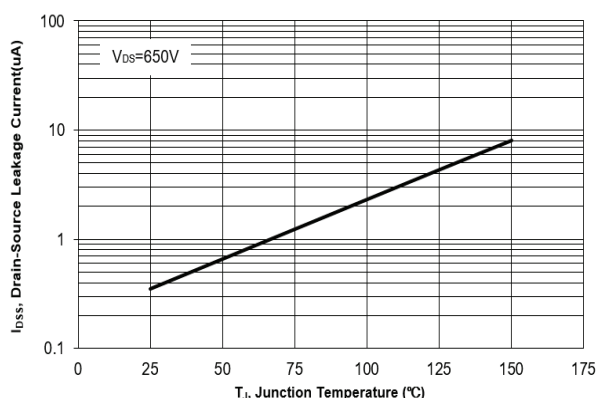


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

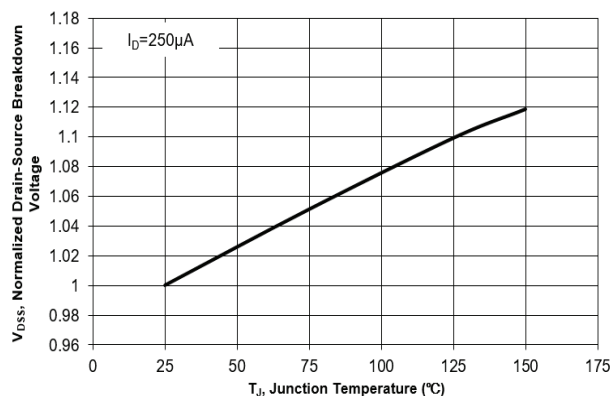


Fig. 10 Gate Threshold Variation vs. T_J

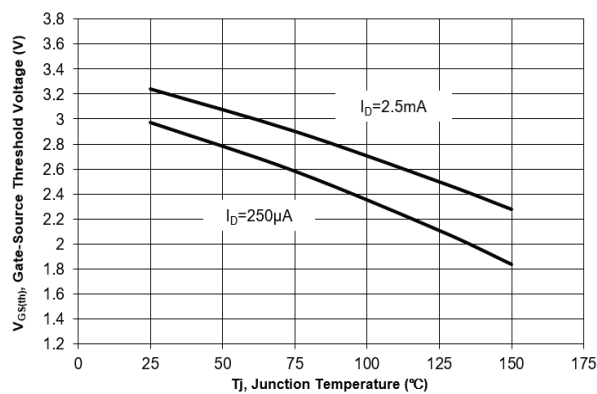


Fig. 11 Gate Charge

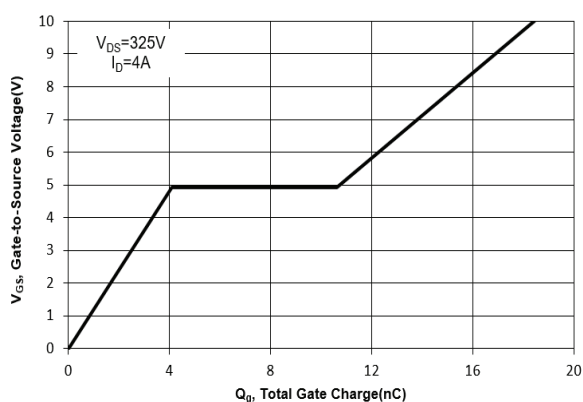


Fig. 12 Safe Operation Area

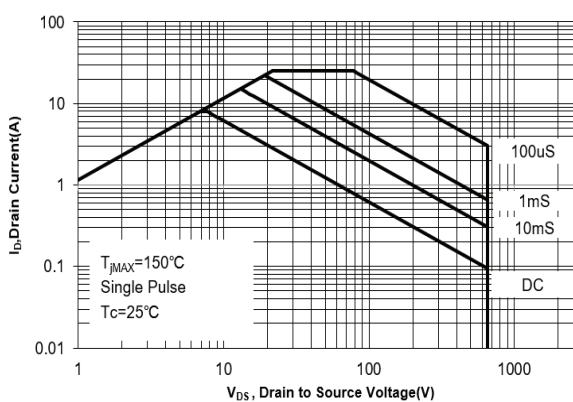


Fig.13 Normalized Maximum Transient Thermal Impedance($z_{\theta JC}$)

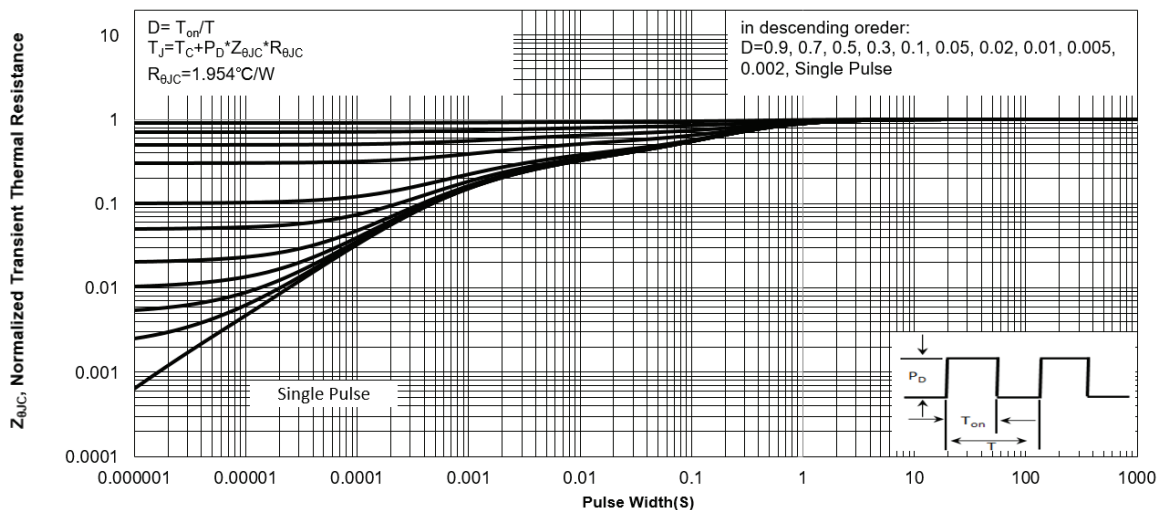
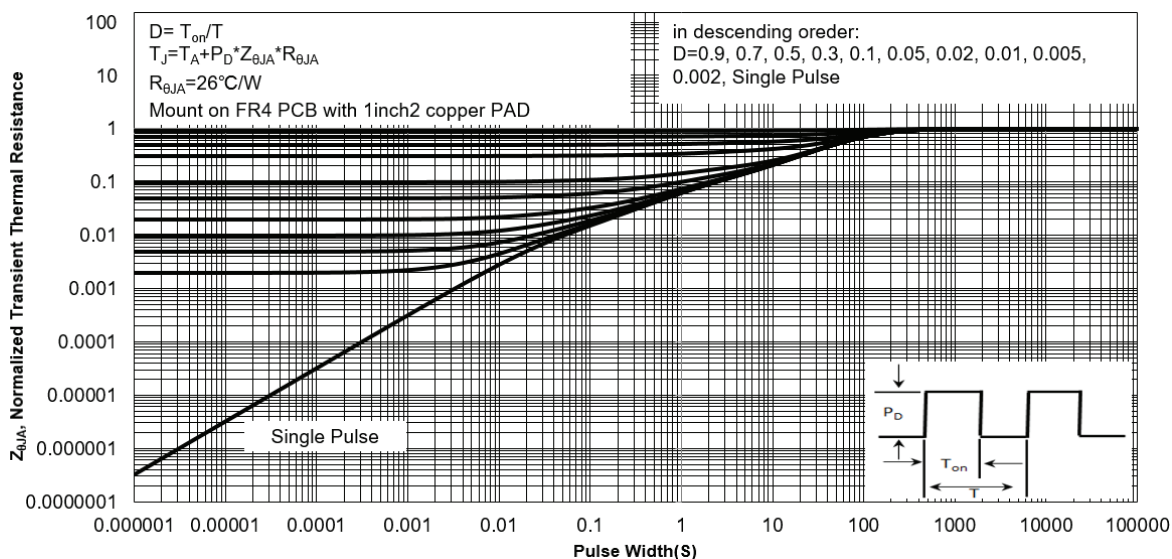


Fig.14 Normalized Maximum Transient Thermal Impedance($z_{\theta JA}$)



Test Circuits

Fig.1-1 Switching times test circuit

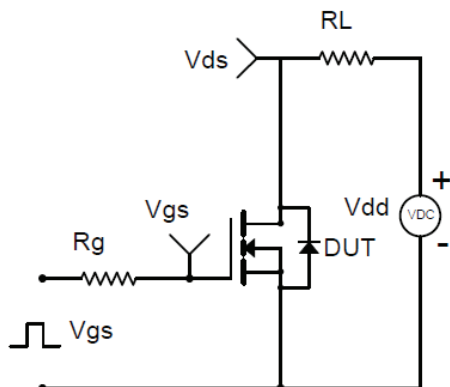


Fig.1-2 Switching Waveform

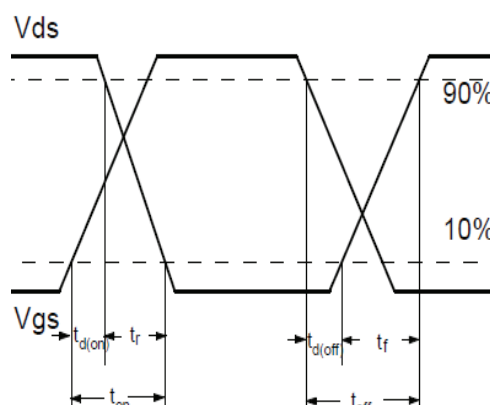


Fig.2-1 Gate charge test circuit

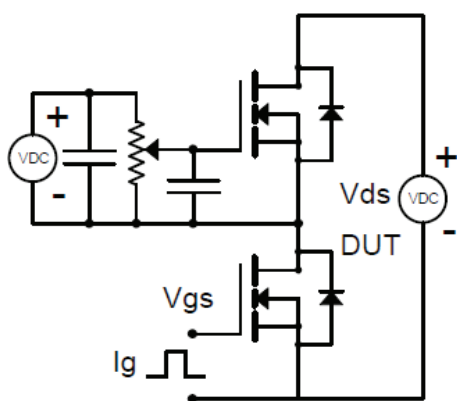


Fig.2-2 Gate charge waveform

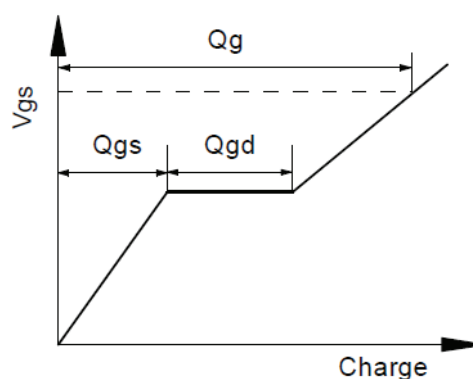


Fig.3-1 Avalanche test circuit

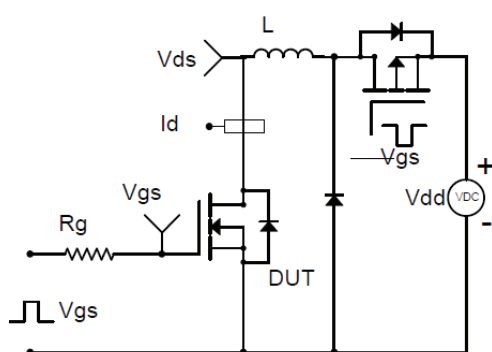
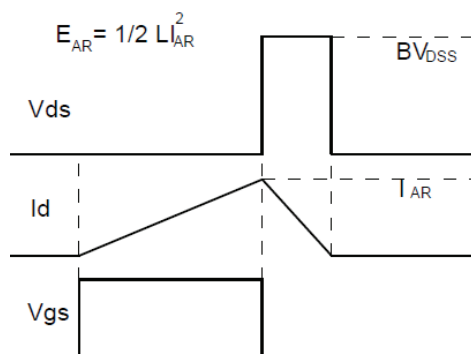
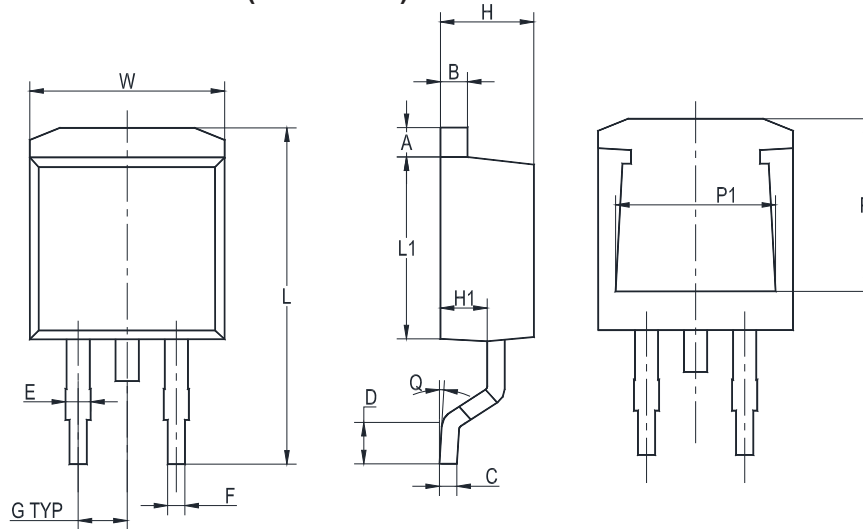
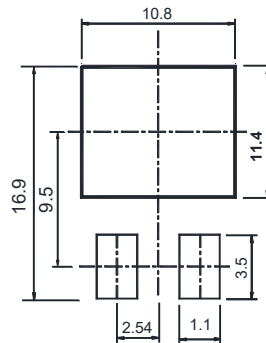


Fig.3-2 Avalanche waveform



Package Outline Dimensions (Units: mm)
TO-263


UNIT	A	B	C	D	E	F	G	W	H	H1	L	L1	Q	P	P1
mm	1.5	1.5	0.5	2.60	1.6	0.94	2.54	10.5	4.8	2.9	16.5	8.7	8°	7.6	8.2
	1.1	1.1	0.3	2.15	1.1	0.68	TYP	9.6	4.4	2.5	14.5	8.2	MAX	7.1	7.4

Recommended Soldering Footprint

Packing information

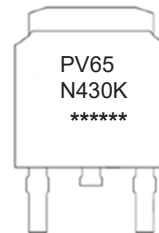
Package	Reel Quantity	Box Quantity	Carton Quantity	Delivery Mode
TO-263	0.8 K / Reel	0.8 K / Box	4K pcs / Carton	Reel

Marking information

" PV65N430K " = Part No.

" ***** " = Date Code Marking

Font type: Arial



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